

Trident: Efficient 4PC Framework for Privacy Preserving Machine Learning

Harsh Chaudhari*, Rahul Rachuri[†], Ajith Suresh*[§]

*Indian Institute of Science, Bangalore, Email: {chaudharim, ajith}@iisc.ac.in

[†]Aarhus University, Denmark, Email: rachuri@cs.au.dk

Abstract—Machine learning has started to be deployed in fields such as healthcare and finance, which involves dealing with a lot of sensitive data. This propelled the need for and growth of privacy-preserving machine learning (PPML). We propose an actively secure four-party protocol (4PC), and a framework for PPML, showcasing its applications on four of the most widely-known machine learning algorithms – Linear Regression, Logistic Regression, Neural Networks, and Convolutional Neural Networks.

Our 4PC protocol tolerating at most one malicious corruption is practically more efficient than Gordon et al. (ASIACRYPT 2018) as the 4th party in our protocol is not active in the online phase, except input sharing and output reconstruction stages. Concretely, we reduce the online communication as compared to them by 1 ring element. We use the protocol to build an efficient mixed-world framework (Trident) to switch between the Arithmetic, Boolean, and Garbled worlds. Our framework operates in the offline-online paradigm over rings and is instantiated in an outsourced setting for machine learning, where the data is secretly shared among the servers. Also, we propose conversions especially relevant to privacy-preserving machine learning. With the privilege of having an extra honest party, we outperform the current state-of-the-art ABY3 (for three parties), in terms of both rounds as well as communication complexity.

The highlights of our framework include using minimal number of expensive circuits overall as compared to ABY3. This can be seen in our technique for truncation, which does not affect the online cost of multiplication and removes the need for any circuits in the offline phase. Our B2A conversion has an improvement of $7\times$ in rounds and $18\times$ in the communication complexity.

The practicality of our framework is argued through improvements in the benchmarking of the aforementioned algorithms when compared with ABY3. All the protocols are implemented over a 64-bit ring in both LAN and WAN settings. Our improvements go up to $187\times$ for the training phase and $158\times$ for the prediction phase when observed over LAN and WAN.

Update: An improved version of this framework appears at [1] <https://eprint.iacr.org/2021/755>

I. INTRODUCTION

Machine learning is one of the fastest-growing research domains today. Applications for machine learning range from smarter keyboard predictions to better object detection in self-driving cars to avoid collisions. This is in part due to more data being made available with the rise of internet companies such as Google and Amazon, as well as due to the machine learning

algorithms themselves getting more robust and accurate. In fact, machine learning algorithms have now started to beat humans at some complicated tasks such as classifying echocardiograms [2], and they are only getting better. Techniques such as deep learning and reinforcement learning are at the forefront making such breakthroughs possible.

The level of accuracy and robustness required is very high to operate in mission-critical fields such as healthcare, where the functioning of the model is vital to the working of the system. Accuracy and robustness are governed by two factors, one of them is the high amount of computing power demanded to train deep learning models. The other factor influencing the accuracy of the model is the variance in the dataset. Variance in datasets comes from collecting data from multiple diverse sources, which is typically infeasible for a single company to achieve.

Towards this, companies such as Microsoft (Azure), Amazon (AWS), Google (Google Cloud), etc. have entered into space by offering “Machine Learning as a Service (MLaaS)”. MLaaS works in two different ways, depending on the end-user. The first scenario is companies offering their trained machine learning models that a customer can query to obtain the prediction result. The second scenario is when multiple customers/companies want to come together and train a common model using their datasets, but none of them wish to share the data in the clear. While promising, both models require the end-user to make compromises. In the case of an individual customer, privacy of his/her query is not maintained and in the case of companies, policies like the European Union General Data Protection Regulation (GDPR) or the EFF’s call for information fiduciary rules for businesses have made it hard and often illegal for companies to share datasets with each other without prior consent of the customers, security, and other criteria met. Even with all these criteria met, data is proprietary information of a company which they would not want to share due to concerns such as competitive advantage.

Due to the huge interest in using machine learning, the field of privacy-preserving machine learning (PPML) has become a fast-growing area of research that addresses the aforementioned problems through techniques for privacy-preserving training and prediction. These techniques when deployed ensure that no information about the query or the datasets is leaked beyond what is permissible by the algorithm, which in some cases might be only the prediction output. Recently there have been a slew of works that have used the techniques of Secure Multiparty Computation (MPC) to perform efficient PPML, works such as [3]–[7] making huge contributions.

Secure multiparty computation is an area of extensive re-

This work was done when Rahul Rachuri was at IIIT-B, India.

[§]Corresponding Author.

search that allows for n mutually distrusting parties to perform computations together on their private inputs, such that no coalition of t parties, controlled by an adversary, can learn any information beyond what is already known and permissible by the algorithm. While MPC has been shown to be practical [8]–[10], MPC for a small number of parties in the *honest majority* setting [11]–[16], [16]–[20] has become popular over the last few years due to applications such as financial data analysis [21], email spam filtering [22], distributed credential encryption [17], privacy-preserving statistical studies [23] that involve only a few parties. This is also evident from popular MPC frameworks such as Sharemind [24] and VIFF [25].

Our Setting: In this work we deal with the specific case of MPC with 4 parties (4PC), tolerating at most 1 malicious corruption. The state-of-the-art three-party (3PC) PPML frameworks in the honest majority setting such as ABY3 [6], SecureNN [7], and ASTRA [26] (prediction only) have fast and efficient protocols for the semi-honest case but are significantly slower when it comes to the malicious setting. This is primarily due to the underlying operations such as Dot Product, Secure Comparison, and Truncation being more expensive in the malicious setting. For instance, the Dot Product protocol of ABY3 incurs communication cost that is linearly dependent on the size of the underlying vector. Since these operations are performed many times, especially during the training phase, more efficient protocols for these operations are crucial in building a better PPML framework.

The motivation behind our 4PC setting is to investigate the performance improvement, both theoretical and practical, over the existing solutions in the 3PC setting, when given the privilege of an additional honest party. We show later in this work that having an extra honest party helps us achieve simpler and much more efficient protocols as compared to 3PC. For instance, operating in 4PC eliminates the need for expensive multiplication triples and allows us to perform a dot product at a cost that is independent of the size of the two vectors.

Our ML constructions are built on a new 4PC scheme, instead of the one proposed by Gordon et al. [27], primarily due to the following reasons:

- 1) Our protocol requires only three out of the four parties to be active during most of the online phase. On the contrary, [27] demands all the four parties to be active during the online phase. Thus, our protocol is more efficient in the setting where the computation is outsourced to a set of servers.
- 2) Using the new secret sharing scheme, our protocol shifts 25% (1 ring element) of the online communication to the offline phase, thus improving the online efficiency.

While our setting is more communication efficient, we assume the presence of an extra honest party which demands an additional 3 pairwise authentic channels when compared to that of 3PC. However, *monetary cost* [28] is an important parameter to look at since the servers need to be running for a long time for complex ML models. The time servers run for and the compute power of the servers dictate the cost of operation. As the fourth party in our framework does not have to be online throughout the online phase, we can shut the server down for most of the online phase. Aided by this fact, the total monetary cost, which would be the total cost of hiring 4 servers to run our framework for either the training or the prediction

phase of an algorithm, we come out ahead of ABY3, primarily because the total running time of the servers in our framework is much lower. More details about monetary cost are presented in Appendix E.

Offline-online paradigm: To improve efficiency, a class of MPC protocols operates in the *offline-online* paradigm [29]. Data-independent computations are carried out in the offline phase, doing so paves way for a fast and efficient online phase of the protocol. Moreover, since the computations performed in the offline phase are data-independent, not all the parties need to be active throughout this phase, placing less reliance on each party. This paradigm has proved its ability to improve the efficiency of protocols in both theoretical [29]–[34] and practical [5], [35]–[42] domains. It is especially useful in a scenario like MLaaS, where the same functions need to be performed many times and the function descriptions are known beforehand. Furthermore, we operate in the outsourced setting of MPC, which allows for an arbitrary number of parties to come together and perform their joint computation via a set of servers. Each server can be thought of as a representative for a subset of data owners, or as an independent party. The advantage of this setting is that it allows the framework to easily scale for a large number of parties and the security notions reduce to that of a standard 4PC between the servers.

Rings vs Fields: In the pursuit of practical efficiency, protocols in MPC that operate over rings are preferred to ones that work over finite fields. This is because of the way computations are carried out in standard 32/64-bit CPUs. Since these architectures have been around for a while, many algorithms are optimized for them. Moreover, operating over rings means that we do not have to override basic operations such as addition and multiplication, unlike with finite fields.

Although MPC techniques have been making a lot of progress towards being practically efficient, we cannot directly use the current best MPC protocols to perform PPML. This is mainly due to two reasons, which are:

1) MPC techniques operate in three different worlds – Arithmetic, Boolean, and Garbled. Each of these worlds is naturally better suited to carry out certain types of computations. For example, the Arithmetic domain (over a ring \mathbb{Z}_{2^t}) is more suited to perform addition whereas the Garbled world is more suited to perform division. Activation functions used in machine learning, such as Rectified Linear Unit (ReLU), have operations that alternate between multiplications and comparisons. Operating in only one of the worlds, as most of the current MPC techniques do, does not give us the maximum possible efficiency. The mixed protocol framework for MPC was first shown to be practical by TASTY [43], which combined Garbled circuits and homomorphic encryption. The idea was later applied to the ML domain by SecureML [3], ABY3 [6] etc., where protocols to switch between the three worlds were proposed. These mixed world frameworks have proven to be orders of magnitude more efficient than operating in a single world.

2) Since most of the computations and intermediate values in machine learning are decimal numbers, we embed them over a ring by allocating the least significant bits to the fractional part. But several multiplications performed may lead to an overflow. A naive solution to avoid this is to use a large ring

to accommodate a fixed number of multiplications, but the number of multiplications for machine learning varies based on the algorithm, making this infeasible. SecureML tackled this problem through truncation, which approximates the value by sacrificing the accuracy by an infinitesimal amount, performed after every multiplication. This technique, however, does not extend into the 3PC or 4PC setting, due to the attack described in ABY3, requiring us to come up with new techniques.

Frameworks such as SecureML and ABY3 have tackled both these issues in the honest majority setting by proposing ways to switch between the three worlds efficiently, as well as efficient ways to do truncation. ABY3 is a lot more efficient than SecureML, in large part due to the 3PC primitives it uses. But ABY3 cannot avoid some expensive operations such as evaluation of a Ripple Carry Adder (RCA) in its truncation and activation functions. Truncation and activation functions – ReLU and Sigmoid, need rounds proportional to the underlying ring size in ABY3. This gives a lot of scope for improvement in the efficiency, which we achieve through our 4PC framework.

A. Our Contribution

We propose an efficient framework for mixed world computations in the four-party honest majority setting with active security over the ring \mathbb{Z}_{2^ℓ} . Our protocols are optimized for PPML and follow the offline-online paradigm. Our improvements come from having an additional honest party in the protocol. Our contributions can be summed up as follows:

1) Efficient 4PC Protocol: We propose an efficient four-party protocol with active security which proceeds through a masked evaluation inspired by Gordon et al. [27]. Our protocol requires 3 ring elements in the online phase per multiplication as opposed to 4 of [27], achieving a 25% improvement. This improvement is achieved by not compromising on the total cost (6 ring elements). Another significant advantage of our protocol is that the fourth party is not required for evaluation in the online phase. This is not the case with [27], where all the parties need to be online throughout the protocol execution. In addition to the stated contributions, our framework also achieves fairness without affecting the complexity of a multiplication gate.

Conversion	Ref.	Rounds	Communication
G2B	ABY3	1	κ
	This	1	3
G2A	ABY3	1	$2\ell\kappa$
	This	1	3ℓ
B2G	ABY3	1	2κ
	This	1	κ
A2G	ABY3	1	$2\ell\kappa$
	This	1	$\ell\kappa$
A2B	ABY3	$1 + \log \ell$	$9\ell \log \ell + 9\ell$
	This	$1 + \log \ell$	$3\ell \log \ell + \ell$
B2A	ABY3	$1 + \log \ell$	$9\ell \log \ell + 9\ell$
	This	1	3ℓ

Table I: Online cost of share conversions of ABY3 [6] and **This** work. ℓ denotes the size of underlying ring in bits and κ denotes the computational security parameter.

2) Fast Mixed World Computation: We propose a framework – Trident, that is geared towards a high throughput online phase as compared to the existing alternatives. This throughput is achieved by making use of an additional honest party. Every one of the conversions we propose to switch between the worlds is more efficient in terms of online communication complexity as compared to ABY3, with our improvements ranging from $2\times$ to $2\kappa/3\times$, where κ denotes the computational security parameter. More concretely, if we aim for 128-bit computational security, our framework gives a maximum improvement of $\approx 85\times$. For instance, the technique we propose to perform bit composition (B2A) requires only 1 round, as opposed to $1 + \log \ell$ rounds in ABY3, which translates to a $7\times$ gain for a 64-bit ring. The table below provides the concrete cost of our online phase in comparison to ABY3. The overall cost comparison can be found in Table IX.

3) Efficient Truncation: The highlight of the protocol we propose for truncation is that it can be combined with our multiplication protocol with no additional cost in the online phase. In contrast, the online cost for multiplication in ABY3 increases from 9 to 12 ring elements, which gives us a $4\times$ improvement in online communication. Moreover, we forgo the need for $(2\ell - 2)$ -round Ripple Carry Adders (RCA), as opposed to ABY3, in the offline phase resulting in an improvement of $63\times$ in rounds for a 64-bit ring.

Conversion	Ref.	Rounds	Communication
Multiplication with Truncation	ABY3	1	12ℓ
	This	1	3ℓ
Secure Comparison	ABY3	$\log \ell$	$18\ell \log \ell$
	This	3	$5\ell + 2$
Bit2A $\llbracket b \rrbracket^B \rightarrow \llbracket b \rrbracket$	ABY3	2	18ℓ
	This	1	3ℓ
BitInj $\llbracket b \rrbracket^B \llbracket v \rrbracket \rightarrow \llbracket bv \rrbracket$	ABY3	3	27ℓ
	This	1	3ℓ
ReLU	ABY3	$3 + \log \ell$	45ℓ
	This	4	$8\ell + 2$
Sigmoid	ABY3	$4 + \log \ell$	$81\ell + 9$
	This	5	$16\ell + 7$

Table II: Online cost of ML conversions of ABY3 [6] and **This** work. ℓ denotes the size of underlying ring in bits.

4) ML Building Blocks: The building blocks for ML highlighted in the Table II (more details in Table X), have improvements ranging from $2\times$ - $3\times$ in the round complexity and $5\times$ - $9\times$ in the communication complexity. For the activation functions ReLU and Sigmoid, our solution brings down the round complexity from $O(\log \ell)$ (of ABY3) to a constant.

5) Implementation: We implement all the stated protocols and test them over LAN and WAN. We benchmark the training and prediction phases of the algorithms – Linear Regression, Logistic Regression, Neural Networks (NN), and Convolutional Neural networks (CNN). In order to compare with ABY3, we implement their protocols as well and obtain the benchmarks in our environment. For the training phase of Linear Regression, we have improvements across different configurations in the range of $2\times$ to $251.84\times$. Similarly, for Logistic Regression, our improvements lie in the range of $2.71\times$ to $67.88\times$. The respective range of improvements for NN and CNN are $2.94\times$ - $68.04\times$ and $3.19\times$ - $45.64\times$. Table III gives the concrete gain

of the aforementioned algorithms over the most widely used MNIST dataset [44], which has 784 features, implemented with a batch size of 128. Moreover, our framework is able to process 23 online iterations of NN in a second for a batch size of 128, over LAN. This is a huge improvement over ABY3, which can process only 2.5 iterations, that too in the semi-honest setting. Similarly, for CNN, we can process 10.46 iterations as opposed to 2 of ABY3.

Network	Linear Regression	Logistic Regression	NN	CNN
LAN	81.08×	27.07×	68.08×	45.64×
WAN	2.17×	2.76×	2.97×	3.19×

Table III: Gain in online throughput for ML Training over ABY3 [6] for $d = 784$ features and batch size of 128.

We also provide results for the prediction phase and give throughput (no. of predictions per second) comparison details for the aforementioned algorithms, using real-world datasets. The gain in online throughput for prediction ranges from $3\times$ to $145.18\times$ for Linear Regression and $3\times$ to $158.40\times$ for Logistic Regression over LAN and WAN combined. Similarly, the online throughput gain ranges from $335.44\times$ to $421.72\times$ for NN and $598.44\times$ to $759.65\times$ for CNN.

II. PRELIMINARIES AND DEFINITIONS

We consider a set of four parties $\mathcal{P} = \{P_0, P_1, P_2, P_3\}$ that are connected by pair-wise private and authentic channels in a synchronous network. The function f to be evaluated is expressed as a circuit ckt, whose topology is publicly known and is evaluated over either an arithmetic ring \mathbb{Z}_{2^ℓ} or a Boolean ring \mathbb{Z}_{2^1} , consisting of 2-input addition and multiplication gates. The term D denotes the multiplicative depth of the circuit, while I, O, A, M denote the number of input wires, output wires, addition gates and multiplication gates respectively in ckt.

We use the notation w_v to denote a wire w with value v flowing through it. We use $g = (w_x, w_y, w_z, \text{op})$ to denote a gate in the ckt with left input wire w_x , right input wire w_y , output wire w_z and operation op , which is either addition (+) or multiplication (\times).

For a vector \vec{x} , x_i denotes the i^{th} element in the vector. For two vectors \vec{x} and \vec{y} of length d , the dot product is given by, $\vec{x} \odot \vec{y} = \sum_{i=1}^d x_i y_i$. Given two matrices \mathbf{X}, \mathbf{Y} , the operation $\mathbf{X} \circ \mathbf{Y}$ denotes the matrix multiplication.

a) Shared Key Setup: In order to facilitate non-interactive communication, parties use functionality $\mathcal{F}_{\text{setup}}$ that establishes pre-shared random keys for a pseudo-random function (PRF) among them. Similar setup for the three-party case can be found in [5], [6], [12], [13], [26].

In our protocols, we make use of a *collision-resistant* hash function, denoted by $H(\cdot)$, to save communication. We defer the formal details of key setup and hash function to Appendix A.

III. OUR 4PC PROTOCOL

In this section, we provide details for our 4PC protocol. We begin with the sharing semantics in Section III-A followed by explaining the relevant building blocks in Section III-B.

We elaborate on the stages of our protocol in Section III-C. Lastly, in Section III-D, we show how to improve the security to achieve fairness.

A. Sharing Semantics

In this section, we explain three variants of secret sharing that are used in this work. The sharings work over both arithmetic (\mathbb{Z}_{2^ℓ}) and boolean (\mathbb{Z}_{2^1}) rings.

a) $[\cdot]$ -sharing: A value v is said to be $[\cdot]$ -shared among parties P_1, P_2, P_3 , if the parties P_1, P_2 and P_3 respectively hold the values v_1, v_2 and v_3 such that $v = v_1 + v_2 + v_3$. We use $[\cdot]_{P_i}$ to denote the $[\cdot]$ -share of party P_i for $i \in \{1, 2, 3\}$.

b) $\langle \cdot \rangle$ -sharing: A value v is said to be $\langle \cdot \rangle$ -shared among parties P_1, P_2, P_3 , if the parties P_1, P_2 and P_3 respectively holds values $(v_2, v_3), (v_3, v_1)$ and (v_1, v_2) such that $v = v_1 + v_2 + v_3$. We denote $\langle \cdot \rangle$ -shares of the parties as follows:

$$\langle v \rangle_{P_1} = (v_2, v_3), \langle v \rangle_{P_2} = (v_3, v_1), \langle v \rangle_{P_3} = (v_1, v_2)$$

c) $\llbracket \cdot \rrbracket$ -sharing: A value v is said to be $\llbracket \cdot \rrbracket$ -shared among parties P_0, P_1, P_2, P_3 , if

- there exist values $\lambda_v, m_v \in \mathbb{Z}_{2^\ell}$ such that $m_v = v + \lambda_v$.
- parties P_1, P_2, P_3 know the value m_v in clear, while the value λ_v is $\langle \cdot \rangle$ -shared among them.
- party P_0 knows $\lambda_{v,1}, \lambda_{v,2}$ and $\lambda_{v,3}$ in clear.

We denote the $\llbracket \cdot \rrbracket$ -shares of the parties as follows:

$$\begin{aligned} \llbracket v \rrbracket_{P_0} &= (\lambda_{v,1}, \lambda_{v,2}, \lambda_{v,3}) & \llbracket v \rrbracket_{P_1} &= (m_v, \lambda_{v,2}, \lambda_{v,3}) \\ \llbracket v \rrbracket_{P_2} &= (m_v, \lambda_{v,3}, \lambda_{v,1}) & \llbracket v \rrbracket_{P_3} &= (m_v, \lambda_{v,1}, \lambda_{v,2}) \end{aligned}$$

We use $\llbracket v \rrbracket = (m_v, \langle \lambda_v \rangle)$ to denote the $\llbracket \cdot \rrbracket$ -share of v .

d) Linearity of the secret sharing schemes: Given the $[\cdot]$ -sharing of x, y and public constants c_1, c_2 , parties can locally compute $[c_1 x + c_2 y] = c_1 [x] + c_2 [y]$.

$$\begin{aligned} [c_1 x + c_2 y] &= (c_1 x_1 + c_2 y_1, c_1 x_2 + c_2 y_2, c_1 x_3 + c_2 y_3) \\ &= c_1 [x] + c_2 [y] \end{aligned}$$

It is easy to see that the linearity trivially extends to $\langle \cdot \rangle$ -sharing as well. That is $\langle c_1 x + c_2 y \rangle = c_1 \langle x \rangle + c_2 \langle y \rangle$. Similarly, given the $\llbracket \cdot \rrbracket$ -sharing of x, y and public constants c_1, c_2 , parties can locally compute $\llbracket c_1 x + c_2 y \rrbracket$. Note that the linearity property enables parties to *non-interactively* evaluate an addition gate as well as perform the multiplication of their shares with a public constant.

B. Building Blocks

a) Sharing Protocol: Protocol Π_{Sh} (Fig. 1) enables party P_i to generate $\llbracket \cdot \rrbracket$ -share of value v . The offline phase is done using the pre-shared keys in such a way that P_i will get the entire mask λ . During the online phase, P_i computes m_v and sends to P_1, P_2, P_3 who exchange the hash values to check for consistency.

Protocol $\Pi_{\text{Sh}}(P_i, v)$

Offline:

- If $P_i = P_0$, parties in $\mathcal{P} \setminus \{P_j\}$ together sample $\lambda_{v,j}$ for $j \in \{1, 2, 3\}$.

- If $P_i = P_k$ for $k \in \{1, 2, 3\}$, parties in \mathcal{P} together sample $\lambda_{v,k}$. In addition, parties in $\mathcal{P} \setminus \{P_j\}$ together sample $\lambda_{v,j}$ for $j \in \{1, 2, 3\} \setminus \{k\}$.

Online:

- P_i computes $m_v = v + \lambda_v$ and sends to P_1, P_2, P_3 .
- P_1, P_2, P_3 mutually exchange $H(m_v)$ and abort if the received values are inconsistent.

Figure 1: $[\![\cdot]\!]$ -sharing of a value v by party P_i .

Looking ahead, we also encounter scenarios where party P_0 has to generate $\langle \cdot \rangle$ -sharing of a value v in the offline phase. We call the resultant protocol as Π_{aSh} and the formal details appear in Fig. 2.

Protocol $\Pi_{aSh}(P_0, v)$

Offline:

- Parties in $\mathcal{P} \setminus \{P_1\}$ sample random $v_1 \in \mathbb{Z}_{2^\ell}$, while parties in $\mathcal{P} \setminus \{P_2\}$ sample random v_2 .
- P_0 computes $v_3 = -(v + v_1 + v_2)$ and sends it to both P_1 and P_2 , who exchange $H(v_3)$ and abort if there is a mismatch.

Figure 2: $\langle \cdot \rangle$ -sharing of a value v by party P_0 .

b) Reconstruction Protocol: Protocol $\Pi_{Rec}(\mathcal{P}, v)$ (Fig. 3) enables parties in \mathcal{P} to compute v , given its $[\![\cdot]\!]$ -share. Towards this, each party receives the missing share from one other party and hash of the missing share from one of the other two parties. If the received shares are consistent, he/she will proceed with the reconstruction. Reconstruction towards a single party can be viewed as a special case of this protocol.

Protocol $\Pi_{Rec}(\mathcal{P}, [\![v]\!])$

Online:

- P_1 receives $\lambda_{v,1}$ and $H(\lambda_{v,1})$ from P_2 and P_0 respectively.
- P_2 receives $\lambda_{v,2}$ and $H(\lambda_{v,2})$ from P_3 and P_0 respectively.
- P_3 receives $\lambda_{v,3}$ and $H(\lambda_{v,3})$ from P_1 and P_0 respectively.
- P_0 receives m_v and $H(m_v)$ from P_1 and P_2 respectively.

P_i for $i \in \{0, 1, 2, 3\}$ abort if the received values are inconsistent. Else computes $v = m_v - \lambda_{v,1} - \lambda_{v,2} - \lambda_{v,3}$.

Figure 3: Reconstruction of value v among parties in \mathcal{P} .

C. Stages of our 4PC protocol

Our protocol Π_{4PC} consists of three stages, namely – Input Sharing, Evaluation and Output Reconstruction. We elaborate on each of these stages below.

a) Input Sharing: For each wire w_v holding the value v , of which $P_i \in \mathcal{P}$ is the owner, he/she generates $[\![\cdot]\!]$ -share of v by executing the $\Pi_{Sh}(P_i, v)$ protocol.

b) Evaluation: In this stage, parties evaluate the circuit in a topological order, where the following invariant is maintained for every gate g : given the inputs of g in $[\![\cdot]\!]$ -shared fashion, the output is generated in the $[\![\cdot]\!]$ -shared fashion. For the case of an addition gate $g = (w_x, w_y, w_z, +)$, the linearity of our sharing scheme maintains this invariant.

For a multiplication gate $g = (w_x, w_y, w_z, \times)$, the protocol proceeds as follows: during the offline phase, parties P_1, P_2, P_3 locally compute $[\![\cdot]\!]$ -shares of $\gamma_{xy} = \lambda_x \lambda_y$, followed by exchanging them to form a $\langle \cdot \rangle$ -sharing of γ_{xy} . Before exchanging the shares of γ_{xy} , the parties randomize the shares by adding a share of 0 to the share of γ_{xy} to prevent leakage. In addition, P_0 helps the parties in verifying the correctness of shares received in the aforementioned step. During the online phase, the goal is to compute m_z . Note that,

$$\begin{aligned} m_z &= z + \lambda_z = xy + \lambda_z = (m_x - \lambda_x)(m_y - \lambda_y) + \lambda_z \\ &= m_x m_y - \lambda_x m_y - \lambda_y m_x + \lambda_x \lambda_y + \lambda_z \end{aligned}$$

Parties P_1, P_2, P_3 locally compute $[\![\cdot]\!]$ -share of $m_z - m_x m_y$ followed by an exchange to reconstruct $m_z - m_x m_y$. By the nature of our secret-sharing scheme, every missing share can be computed by two parties. This facilitates the parties to verifiably reconstruct $m_z - m_x m_y$ by having one party send the missing share and the other send a hash of the same.

Each of P_1, P_2, P_3 locally add $m_x m_y$ to the result to obtain m_z . We call the resultant protocol Π_{Mult} (Fig. 4).

Protocol $\Pi_{Mult}(w_x, w_y, w_z)$

Offline:

- Parties in $\mathcal{P} \setminus \{P_j\}$ together sample $\lambda_{z,j}$ for $j \in \{1, 2, 3\}$.
- Parties invoke protocol Π_{Zero} (Fig. 21) to generate A, B, Γ such that $A + B + \Gamma = 0$. Parties locally compute the following:
 - P_0, P_1 compute $\gamma_{xy,2} = \lambda_{x,2} \lambda_{y,2} + \lambda_{x,2} \lambda_{y,3} + \lambda_{x,3} \lambda_{y,2} + A$.
 - P_0, P_2 compute $\gamma_{xy,3} = \lambda_{x,3} \lambda_{y,3} + \lambda_{x,3} \lambda_{y,1} + \lambda_{x,1} \lambda_{y,3} + B$.
 - P_0, P_3 compute $\gamma_{xy,1} = \lambda_{x,1} \lambda_{y,1} + \lambda_{x,1} \lambda_{y,2} + \lambda_{x,2} \lambda_{y,1} + \Gamma$.
- Parties exchange the following:
 - P_1 receives $\gamma_{xy,3}$ and $H(\gamma_{xy,3})$ from P_2 and P_0 respectively.
 - P_2 receives $\gamma_{xy,1}$ and $H(\gamma_{xy,1})$ from P_3 and P_0 respectively.
 - P_3 receives $\gamma_{xy,2}$ and $H(\gamma_{xy,2})$ from P_1 and P_0 respectively.
- P_i for $i \in \{1, 2, 3\}$ abort if received values are inconsistent.

Online: Let $m'_z = m_z - m_x m_y$.

- Parties locally compute the following:
 - P_1, P_3 compute $m'_{z,2} = -\lambda_{x,2} m_y - \lambda_{y,2} m_x + \gamma_{xy,2} + \lambda_{z,2}$.
 - P_2, P_1 compute $m'_{z,3} = -\lambda_{x,3} m_y - \lambda_{y,3} m_x + \gamma_{xy,3} + \lambda_{z,3}$.
 - P_3, P_2 compute $m'_{z,1} = -\lambda_{x,1} m_y - \lambda_{y,1} m_x + \gamma_{xy,1} + \lambda_{z,1}$.
- Parties exchange the following:
 - P_1 receives $m'_{z,1}$ and $H(m'_{z,1})$ from P_2 and P_3 respectively.
 - P_2 receives $m'_{z,2}$ and $H(m'_{z,2})$ from P_3 and P_1 respectively.
 - P_3 receives $m'_{z,3}$ and $H(m'_{z,3})$ from P_1 and P_2 respectively.
- P_i for $i \in \{1, 2, 3\}$ abort if the received values are inconsistent. Else, he / she computes $m_z = (m'_{z,1} + m'_{z,2} + m'_{z,3}) + m_x m_y = m'_z + m_x m_y$.

Figure 4: Multiplication Protocol.

As a very important optimization, note that the exchange of hash values for every multiplication gate can be delayed until the output reconstruction stage. Moreover, all the corresponding values can be appended and hashed, resulting in an overall communication of only 3 ring elements.

c) Output Reconstruction: For each of the output wire w_y with value y , parties execute protocol $\Pi_{Rec}(\mathcal{P}, [\![y]\!])$ to reconstruct the output.

Correctness and Security: We prove the correctness of Π_{4PC} below and defer the security details to Appendix F.

Theorem III.1 (Correctness). *Protocol Π_{4PC} is correct.*

Proof: We claim that for every wire in ckt, the parties hold a $[\![\cdot]\!]$ -sharing of the wire value in Π_{4PC} . The correctness for the input and output wires follows from Π_{Sh} and Π_{Rec} respectively. The claim for addition gates follows from the linearity of $[\![\cdot]\!]$ -sharing. For a multiplication gate $g = (w_x, w_y, w_z, \times)$, when evaluated using Π_{Mult} , the parties receive $xy + \lambda_z$ in the online phase, resulting in obtaining $[\![z]\!]$. The correctness of $xy + \lambda_z$ is ensured through the verified reconstruction as shown in Π_{Mult} . ■

Theorem III.2 (Communication Efficiency). *Π_{4PC} requires one round with an amortized communication of $3M$ ring elements during the offline phase. In the online phase, Π_{4PC} requires one round with an amortized communication of at most $3l$ ring elements in the Input-sharing stage, D rounds with an amortized communication of $3M$ ring elements for evaluation stage and one round with an amortized communication of $3O$ elements for the output-reconstruction stage.*

D. Achieving Fairness

For fairness, we need to ensure that all the parties are *alive* in the protocol during the output reconstruction stage. On top of this, we also need to prevent the adversary from mounting a selective abort attack, where he can make some of the honest parties abort the protocol. To achieve this, parties P_1, P_2, P_3 set a bit b to *continue*, if the verification of the multiplication gates was successful, else set it to *abort*, and send it to P_0 . P_0 then sends *abort* back to all the parties if one of the parties sends *abort*, thus ensuring aliveness. Remaining parties then exchange their reply from P_0 and follow the honest-majority in deciding whether to proceed or abort. Since there can be only 1 corruption, all the parties will now be on the same page, preventing a selective abort. If the parties decide to proceed, they exchange the missing shares. Using the fact that there is at most 1 corruption and the structure of our secret-sharing scheme, the most commonly received missing share will be consistent among the honest parties.

Protocol $\Pi_{Rec}(\mathcal{P}, [\![v]\!])$

Online:

- P_1, P_2, P_3 set bit $b = \text{abort}$ if the verification for multiplication fails. Else set $b = \text{continue}$.
- P_1, P_2, P_3 send b to P_0 who sends back *abort*, if he/she receives at least one *abort* bit. Else sends *continue* to P_1, P_2, P_3 .
- P_1, P_2, P_3 mutually exchange the message received from P_0 . Parties *abort* if the majority of the messages received are *abort*. Else they exchange the missing share as follows:
 - P_0 receives m_v from P_1, P_2 and $H(m_v)$ from P_3 respectively.
 - P_1 receives $\lambda_{v,1}$ from P_2, P_3 and $H(\lambda_{v,1})$ from P_0 respectively.
 - P_2 receives $\lambda_{v,2}$ from P_3, P_1 and $H(\lambda_{v,2})$ from P_0 respectively.
 - P_3 receives $\lambda_{v,3}$ from P_1, P_2 and $H(\lambda_{v,3})$ from P_0 respectively.

- P_i for $i \in \{0, 1, 2, 3\}$ chooses the missing share that forms the majority and computes $v = m_v - \lambda_{v,1} - \lambda_{v,2} - \lambda_{v,3}$.

Figure 5: Fair reconstruction of value v among parties in \mathcal{P} .

IV. MIXED PROTOCOL FRAMEWORK

In this section, we present our mixed protocol framework, Trident. Before we go into the details of it, we discuss another world of MPC, called The Garbled World. To evaluate circuits over a ring \mathbb{Z}_{2^ℓ} , we operate in the arithmetic world and to evaluate boolean circuits (\mathbb{Z}_2^ℓ) we use either the boolean world or the Garbled world, depending on the operation being performed. Superscripts $\{\mathbf{A}, \mathbf{B}, \mathbf{G}\}$ are used to indicate the respective worlds. If there is no superscript, the values are assumed to operate in the arithmetic world.

A. The Garbled World

For the Garbled world, we use the MRZ [17] scheme in the 4PC setting. In 4PC, parties P_1, P_2, P_3 act as the garblers and the party P_0 acts as the sole evaluator. As an optimisation, P_0 can share his inputs with only P_1, P_2 instead of all three parties. For cross-verification, P_1 sends the garbled circuit to P_0 while P_2 sends a hash of the it to P_0 . We incorporate the recent optimisations including free XOR [45], [46], half-gates [47], [48], fixed-key AES garbling [49]. As opposed to the dishonest majority setting, this scheme removes the need for expensive public key primitives (in terms of communication) such as Oblivious Transfers altogether. We present the protocols for a single bit, and each operation can be performed ℓ times in parallel to support ℓ -bit values.

a) Sharing Semantics: For a bit v , $[\![v]\!]^G$ is defined as $[\![v]\!]_{P_i}^G = K_v^0 \in \{0, 1\}^\kappa$ for $i \in \{1, 2, 3\}$ and $[\![v]\!]_{P_0}^G = K_v^1 = K_v^0 \oplus vR$, where κ is the computational security parameter. Here R is a global *offset* with the least significant bit as one, and is known only to P_1, P_2, P_3 (generated by shared randomness), and R is common across all the $[\![\cdot]\!]^G$ -sharing. It is easy to see that XOR of the least significant bit of $[\![v]\!]_{P_1}^G$ (resp. $[\![v]\!]_{P_2}^G, [\![v]\!]_{P_3}^G$) and $[\![v]\!]_{P_0}^G$ is v . For a value $v \in \mathbb{Z}_{2^\ell}$, we abuse the notation $[\![v]\!]^G$ to denote the set of $[\![\cdot]\!]^G$ -shares of each bit of v .

Protocol $\Pi_{Sh}^G(P_i, v)$

Offline:

- P_1, P_2, P_3 samples a random $K_v^0 \in \{0, 1\}^\kappa$, computes $K_v^1 = K_v^0 \oplus R$ and set $[\![v]\!]_{P_1}^G = [\![v]\!]_{P_2}^G = [\![v]\!]_{P_3}^G = K_v^0$.
- P_1, P_2, P_3 compute commitment of K_v^0, K_v^1 . P_1, P_2 send the commitments to P_0 in a random permuted order, who *abort* if the received commitments mismatch.

Online:

- P_i sends $K_v^0 \oplus vR$ to P_0 , who sets it as $[\![v]\!]_{P_0}^G$.
- P_i decommits the right key K_v^1 to P_0 , who *abort* if the decommitment is incorrect.

Figure 6: $[\![\cdot]\!]^G$ -sharing of v by P_i for $i \in \{1, 2, 3\}$.

b) Input Sharing: Protocol $\Pi_{\text{Sh}}^G(P_i, v)$ enables P_i to generate $\llbracket \cdot \rrbracket^G$ -sharing of value v . During the protocol, P_0 needs to ensure that it obtains the correct K_v^v . To tackle this, we make the garblers commit both keys to P_0 , who can then verify the correctness by cross-checking the commitments received. The formal details for the case when P_i is one of the garblers appear in Fig. 6.

If $P_i = P_0$, then $\Pi_{\text{Sh}}^G(P_i, v)$ proceeds as follows: P_0 samples random bit v_1 , computes $v_2 = v \oplus v_1$ and sends v_1 and v_2 to P_1 and P_2 respectively. Parties execute $\Pi_{\text{Sh}}^G(P_1, v_1)$ and $\Pi_{\text{Sh}}^G(P_2, v_2)$ to generate $\llbracket v_1 \rrbracket^G$ and $\llbracket v_2 \rrbracket^G$ respectively. Parties then locally compute $\llbracket v \rrbracket^G = \llbracket v_1 \rrbracket^G \oplus \llbracket v_2 \rrbracket^G$, using the XOR gate evaluation method via the free-XOR technique. Here, the commitments of the keys need not be permuted, as P_0 already knows the actual v_1 and v_2 . As an optimization, the computation of $\llbracket v_1 \rrbracket^G$ can be offloaded to the offline phase.

c) Reconstruction: If $P_i = P_0$, then P_1, P_2 send the least significant bit of their shares and P_i verifies if it received the same bit from both P_1 and P_2 . If P_i is one of the garblers, then P_0 sends its share to P_i . Due to the *authenticity* of the underlying garbling scheme [50], a corrupt P_0 cannot send an incorrect share to P_i . If there are multiple reconstructions towards $P_i \in \{P_1, P_2, P_3\}$, P_0 can send the least significant bit of its shares along with a hash of all the corresponding shares.

d) Operations: Let $u, v \in \{0, 1\}$ be $\llbracket \cdot \rrbracket^G$ -shared with P_1, P_2, P_3 holding the shares (K_u^0, K_v^0) , and P_0 holding the shares $(K_u^0 \oplus uR, K_v^0 \oplus vR)$. Let c denote the output.

- *XOR:* The parties locally compute $\llbracket c \rrbracket^G = \llbracket u \rrbracket^G \oplus \llbracket v \rrbracket^G$.
- *AND:* P_1, P_2, P_3 sample random $K_c^0 \in \{0, 1\}^\kappa$, compute $K_u^1 = K_u^0 \oplus R$ and construct a garbled table for AND using the garbling scheme described in [47], [49]. P_1 sends the garbled table to P_0 , while P_2 sends a hash of the table to P_0 . P_0 evaluates the table¹ to obtain $\llbracket c \rrbracket_{P_0}^G = K_c^c$. P_i for $i \in \{1, 2, 3\}$ sets $\llbracket c \rrbracket_{P_i}^G = K_c^0$.

B. Building Blocks

a) Verifiable Arithmetic/Boolean Sharing: Protocol Π_{vSh} (Fig. 7) allows two parties P_i, P_j to generate $\llbracket \cdot \rrbracket$ -sharing of value v in a verifiable manner. On a high level, P_i executes Π_{Sh} on v , while P_j helps in verification by sending $H(m_v)$ to parties P_1, P_2, P_3 .

Protocol $\Pi_{\text{vSh}}(P_i, P_j, v)$

Offline: Parties execute offline steps of $\Pi_{\text{Sh}}(P_i, v)$.

Online:

- P_i computes $m_v = v + \lambda_v$ and sends to P_1, P_2, P_3 .
- P_j computes $H(m_v)$ and sends to P_1, P_2, P_3 , who **abort** if the received values are inconsistent.

Figure 7: Verifiable Arithmetic/Boolean sharing of a value v .

We observe that the parties can *non-interactively* generate $\llbracket \cdot \rrbracket$ -sharing of a value v when all of the parties P_1, P_2, P_3 know

¹The garbled table can be sent during the offline phase, while P_0 needs to evaluate the garbled circuit during the online phase.

v . Parties set $\lambda_{v,1} = \lambda_{v,2} = \lambda_{v,3} = 0$ and $m_v = v$. We abuse the notation and use $\Pi_{\text{vSh}}(P_1, P_2, P_3, v)$ to denote this protocol.

b) Verifiable Garbled Sharing: Protocol Π_{vSh}^G (Fig. 8) is adapted from ABY3 [6] and allows two parties P_i, P_j to generate $\llbracket \cdot \rrbracket^G$ -sharing of value v in a verifiable manner. When P_i, P_j are both garblers, one of them can send the key while the other can send just the hash to check for inconsistency. If $P_0 = P_j$, the other parties (P_1, P_2) send commitments of the keys in order, to P_0 . In addition, P_i sends the decommitment of the actual key to P_0 .

Protocol $\Pi_{\text{vSh}}^G(P_i, P_j, v)$

Offline: P_1, P_2, P_3 locally sample random $K_v^0 \in \{0, 1\}^\kappa$, compute $K_v^1 = K_v^0 \oplus R$ and set $\llbracket v \rrbracket_{P_1}^G = \llbracket v \rrbracket_{P_2}^G = \llbracket v \rrbracket_{P_3}^G = K_v^0$.

Online:

- **If** $(P_i, P_j) = (P_1, P_0)$:
 - P_1, P_2 compute commitments $\text{Com}(K_v^0), \text{Com}(K_v^1)$ and send it to P_0 . In addition, P_1 sends decommitment of $\text{Com}(K_v^0)$ to P_0 .
 - P_0 **abort** if either the received commitments are inconsistent or the decommitment is incorrect. Else he/she sets $\llbracket v \rrbracket_{P_0}^G = K_v^v$.
- **If** $(P_i, P_j) = (P_k, P_0)$ **for** $k \in \{2, 3\}$: The steps are similar as above.
- **If** $(P_i, P_j) = (P_1, P_2)$: P_1 and P_2 sends K_v^v and $H(K_v^v)$ respectively to P_0 , who **abort** if the received values are inconsistent. Else he/she sets $\llbracket v \rrbracket_{P_0}^G = K_v^v$.
- **If** $(P_i, P_j) = (P_1, P_3)$ **or** (P_2, P_3) : The steps are similar as above.

Figure 8: Verifiable Garbled sharing of a value v .

c) Dot Product: Given two vectors \vec{x} and \vec{y} , each of size d , Π_{DotP} (Fig. 9) computes the dot product $z = \vec{x} \odot \vec{y}$. ABY3 [6] and ASTRA [26] have proposed efficient dot product protocols for the semi-honest and malicious settings. In the semi-honest case, their dot product cost is the same as a single multiplication, but for the malicious case it scales with the vector size. In comparison, the cost of our dot product is independent of the vector size. On a high level, instead of performing reconstruction for each multiplication $x_j \cdot y_j$ for $j \in \{1, \dots, d\}$, parties locally add their shares corresponding to all the multiplications and perform a single exchange.

Protocol $\Pi_{\text{DotP}}(\vec{x}, \vec{y})$

Let $z = \vec{x} \odot \vec{y}$.

Offline:

- Parties in $\mathcal{P} \setminus \{P_j\}$ together sample $\lambda_{z,j}$ for $j \in \{1, 2, 3\}$.
- Parties invoke protocol Π_{Zero} (Fig. 21) to generate A, B, Γ such that $A + B + \Gamma = 0$. Parties locally compute the following:
 - P_0, P_1 compute $\gamma_{xy,2} = \sum_{j=1}^d \gamma_{x_j y_j, 2} = \sum_{j=1}^d (\lambda_{x_j, 2} \lambda_{y_j, 2} + \lambda_{x_j, 2} \lambda_{y_j, 3} + \lambda_{x_j, 3} \lambda_{y_j, 2}) + A$.
 - P_0, P_2 compute $\gamma_{xy,3} = \sum_{j=1}^d \gamma_{x_j y_j, 3} = \sum_{j=1}^d (\lambda_{x_j, 3} \lambda_{y_j, 3} + \lambda_{x_j, 3} \lambda_{y_j, 1} + \lambda_{x_j, 1} \lambda_{y_j, 3}) + B$.
 - P_0, P_3 compute $\gamma_{xy,1} = \sum_{j=1}^d \gamma_{x_j y_j, 1} = \sum_{j=1}^d (\lambda_{x_j, 1} \lambda_{y_j, 1} + \lambda_{x_j, 1} \lambda_{y_j, 2} + \lambda_{x_j, 2} \lambda_{y_j, 1}) + \Gamma$.
- Parties exchange the following:

- P_1 receives $\gamma_{xy,3}$ and $H(\gamma_{xy,3})$ from P_2 and P_0 respectively.
- P_2 receives $\gamma_{xy,1}$ and $H(\gamma_{xy,1})$ from P_3 and P_0 respectively.
- P_3 receives $\gamma_{xy,2}$ and $H(\gamma_{xy,2})$ from P_1 and P_0 respectively.
- P_i for $i \in \{1, 2, 3\}$ abort if the received values are inconsistent.

Online: Let $m'_z = m_z - \sum_{j=1}^d m_{x_j} m_{y_j}$.

- Parties locally compute the following:
 - P_1, P_3 : $m'_{z,2} = \sum_{j=1}^d (-\lambda_{x_j,2} m_{y_j} - \lambda_{y_j,2} m_{x_j}) + \gamma_{xy,2} + \lambda_{z,2}$.
 - P_2, P_1 : $m'_{z,3} = \sum_{j=1}^d (-\lambda_{x_j,3} m_{y_j} - \lambda_{y_j,3} m_{x_j}) + \gamma_{xy,3} + \lambda_{z,3}$.
 - P_3, P_2 : $m'_{z,1} = \sum_{j=1}^d (-\lambda_{x_j,1} m_{y_j} - \lambda_{y_j,1} m_{x_j}) + \gamma_{xy,1} + \lambda_{z,1}$.
- Parties exchange the following:
 - P_1 receives $m'_{z,1}$ and $H(m'_{z,1})$ from P_2 and P_3 respectively.
 - P_2 receives $m'_{z,2}$ and $H(m'_{z,2})$ from P_3 and P_1 respectively.
 - P_3 receives $m'_{z,3}$ and $H(m'_{z,3})$ from P_1 and P_2 respectively.
- P_i for $i \in \{1, 2, 3\}$ abort if the received values are inconsistent. Else, he / she computes $m_z = (m'_{z,1} + m'_{z,2} + m'_{z,3}) + \sum_{j=1}^d (m_{x_j} m_{y_j}) = m'_z + \sum_{j=1}^d (m_{x_j} m_{y_j})$.

Figure 9: Dot Product Protocol.

C. Sharing Conversions

We now discuss the inter-sharing conversions among Arithmetic, Boolean, and Garbled sharing.

a) Garbled to Boolean Sharing (G2B): To convert a garbled share into the boolean world, P_1, P_2 first generate the garbled and boolean shares of a random value (r) using their shared randomness in the offline phase. In addition, they communicate the garbled circuit which performs the XOR of two bits along with the decoding information (Note that the garbled circuit does not have to be communicated due to the free XOR technique). In the online phase, P_0 evaluates and obtains $v \oplus r$ and sends it to P_3 along with the hash of the corresponding key. Authenticity of the underlying garbling scheme ensures that a corrupt P_0 cannot send the wrong bit, as he will not be able to guess the right key for it.

Protocol Π_{G2B}

Offline:

- P_1, P_2 locally sample random $r \in \mathbb{Z}_{2^\ell}$. Parties execute $\Pi_{vSh}^G(P_1, P_2, r)$ and $\Pi_{vSh}^B(P_1, P_2, r)$ to generate $\llbracket r \rrbracket^G$ and $\llbracket r \rrbracket^B$ respectively.
- P_1, P_2, P_3 garble a boolean adder circuit $Add(x, y)$ that computes $x \oplus y$. P_1 sends the garbled circuit along with the decoding information to P_0 , while P_2 sends a combined hash to P_0 .

Online:

- P_0 evaluates the circuit Add on v and r to obtain $v \oplus r$. P_0 sends $v \oplus r$ along with a hash of the actual key corresponding to $v \oplus r$ to P_3 . P_3 abort if the received values are inconsistent.
- Else, parties execute $\Pi_{vSh}^B(P_3, P_0, v \oplus r)$ to generate $\llbracket v \oplus r \rrbracket^B$.
- Parties locally compute $\llbracket v \rrbracket^B = \llbracket v \oplus r \rrbracket^B \oplus \llbracket r \rrbracket^B$.

Figure 10: Garbled to Boolean Sharing.

b) Garbled to Arithmetic Sharing (G2A): This conversion proceeds in a similar way as Π_{G2B} . The major difference is that instead of the garbled circuit for XOR, the parties communicate a circuit for subtraction of two ℓ -bit values. Note that P_0 needs to communicate only a single hash combining all the keys corresponding to the ℓ -bits of $v - r$.

Protocol Π_{G2A}

Offline:

- P_1, P_2 locally sample random $r \in \mathbb{Z}_{2^\ell}$. Parties execute $\Pi_{vSh}^G(P_1, P_2, r)$ and $\Pi_{vSh}^A(P_1, P_2, r)$ to generate $\llbracket r \rrbracket^G$ and $\llbracket r \rrbracket^A$ respectively.
- P_1, P_2, P_3 garble a subtractor circuit $Sub(x, y)$ that computes $x - y$. P_1 sends the garbled circuit along with the decoding information to P_0 , while P_2 sends a combined hash to P_0 , who abort if the received values are inconsistent.

Online:

- P_0 evaluates the circuit Sub on v and r to obtain $v - r$. P_0 sends $v - r$ along with a combined hash of all the actual keys corresponding to $v - r$ to P_3 . P_3 abort if the received values are inconsistent.
- Else, parties execute $\Pi_{vSh}^A(P_3, P_0, v - r)$ to generate $\llbracket v - r \rrbracket^A$.
- Parties locally compute $\llbracket v \rrbracket^A = \llbracket v - r \rrbracket^A + \llbracket r \rrbracket^A$.

Figure 11: Garbled to Arithmetic Sharing.

c) Boolean to Garbled Sharing (B2G): Since the bit $v = (m_v \oplus \lambda_{v,1}) \oplus (\lambda_{v,2} \oplus \lambda_{v,3})$ in the boolean world, if we can get the garbled shares of $x = (m_v \oplus \lambda_{v,1})$ and $y = (\lambda_{v,2} \oplus \lambda_{v,3})$, parties can use the free XOR technique to compute the garbled shares of v locally. Each of x, y is possessed by two parties, enabling them to verifiably generate the garbled shares using the protocol Π_{vSh}^G .

Protocol Π_{B2G}

Offline: P_0, P_1 execute $\Pi_{vSh}^G(P_1, P_0, y)$ to generate $\llbracket y \rrbracket^G$ where $y = \lambda_{v,2} \oplus \lambda_{v,3}$.

Online:

- P_2, P_3 execute $\Pi_{vSh}^G(P_2, P_3, x)$ to generate $\llbracket x \rrbracket^G$ where $x = m_v \oplus \lambda_{v,1}$.
- Parties locally compute $\llbracket v \rrbracket^G = \llbracket x \rrbracket^G \oplus \llbracket y \rrbracket^G$.

Figure 12: Boolean to Garbled Sharing.

d) Arithmetic to Garbled Sharing (A2G): Similar to Π_{B2G} , $v = (m_v - \lambda_{v,1}) - (\lambda_{v,2} + \lambda_{v,3})$ and the parties can verifiably generate the garbled shares of $x = (m_v - \lambda_{v,1})$ and $y = (\lambda_{v,2} + \lambda_{v,3})$ using Π_{vSh}^G . In the online phase, the parties evaluate a garbled subtractor circuit to obtain the shares of $v = x - y$.

Protocol Π_{A2G}

Offline:

- P_0, P_1 execute $\Pi_{vSh}^G(P_1, P_0, y)$ to generate $\llbracket y \rrbracket^G$ where $y = \lambda_{v,2} + \lambda_{v,3}$.

- P_1, P_2, P_3 garble a subtractor circuit $\text{Sub}(x, y)$ that computes $x - y$. P_1 sends the garbled circuit to P_0 , while P_2 sends a hash of the same to P_0 , who **abort** if the received values are inconsistent.

Online:

- P_2, P_3 execute $\Pi_{\text{vSh}}^G(P_2, P_3, x)$ to generate $\llbracket x \rrbracket^G$ where $x = m_v - \lambda_{v,1}$.
- Parties compute $\llbracket v \rrbracket^G = \llbracket x \rrbracket^G - \llbracket y \rrbracket^G$ by evaluating circuit Sub .

Figure 13: Arithmetic to Garbled Sharing.

e) *Arithmetic to Boolean Sharing (A2B)*: This conversion proceeds similarly to Π_{A2G} , the only difference being parties now generate boolean shares of $x = (m_v - \lambda_{v,1})$ and $y = (\lambda_{v,2} + \lambda_{v,3})$ and evaluate a boolean subtractor circuit instead to compute boolean shares of $v = x - y$.

Protocol Π_{A2B}

Offline: P_0, P_1 execute $\Pi_{\text{vSh}}^B(P_1, P_0, y)$ to generate $\llbracket y \rrbracket^B$ where $y = \lambda_{v,2} + \lambda_{v,3}$.

Online:

- P_2, P_3 execute $\Pi_{\text{vSh}}^B(P_2, P_3, x)$ to generate $\llbracket x \rrbracket^B$ where $x = m_v - \lambda_{v,1}$.
- Parties compute $\llbracket v \rrbracket^B = \llbracket x \rrbracket^B - \llbracket y \rrbracket^B$ by evaluating an ℓ -bit Boolean subtractor circuit Sub .

Figure 14: Arithmetic to Boolean Sharing.

f) *Bit to Arithmetic Sharing (Bit2A)*: Let u and v denote the bits λ_b and m_b respectively over the ring \mathbb{Z}_{2^ℓ} . Then,

$$b = m_b \oplus \lambda_b = v + u - 2vu$$

Party P_0 generates $\langle \cdot \rangle$ -shares of u in the offline phase. To ensure the correctness of the shares, parties P_1, P_2, P_3 check whether the following equation holds – $(\lambda_b \oplus r_b)' = u + r_b' - 2ur_b'$ where the superscript $(\cdot)'$ denotes the corresponding bits over ring \mathbb{Z}_{2^ℓ} . After the verification, parties locally convert $\langle u \rangle$ to $\llbracket u \rrbracket$. In the online phase, parties multiply $\llbracket u \rrbracket, \llbracket v \rrbracket$ to generate $\llbracket uv \rrbracket$ followed by locally computing $\llbracket b \rrbracket = \llbracket v \rrbracket + \llbracket u \rrbracket - 2\llbracket uv \rrbracket$. Note that since λ_v is set to 0 while executing $\Pi_{\text{Sh}}(P_1, P_2, P_3, v)$ protocol, γ_{uv} -sharing is not needed during multiplication.

Protocol Π_{Bit2A}

Offline: Let u and $\lambda_{u,i}$ denote the bits λ_b and $\lambda_{b,i}$ respectively over the ring \mathbb{Z}_{2^ℓ} . Here $i \in \{1, 2, 3\}$.

- P_0 executes $\Pi_{\text{aSh}}(P_0, u)$ (Fig. 2) to generate $\langle u \rangle$. Let the shares be $\langle u \rangle_{P_1} = (u_2, u_3)$, $\langle u \rangle_{P_2} = (u_3, u_1)$, and $\langle u \rangle_{P_3} = (u_1, u_2)$.
- P_1, P_2, P_3 performs the following check:
 - P_1, P_2 sample a random ring element r and a random bit r_b . Let r_b' denotes the bit r_b over ring \mathbb{Z}_{2^ℓ} .
 - P_1 computes $x_1 = \lambda_{b,3} \oplus r_b, y_1 = (u_2 + u_3)(1 - 2r_b') + r_b' + r$ and sends (x_1, y_1) to P_3 .
 - P_2 computes $y_2 = u_1(1 - 2r_b') - r$ and sends $H(y_2)$ to P_3 .
 - P_3 computes $x = \lambda_b \oplus r_b = x_1 \oplus \lambda_{b,1} \oplus \lambda_{b,2}$ and **abort** if $H(x' - y_1) \neq H(y_2)$. Here x' denotes the bit x over ring \mathbb{Z}_{2^ℓ} .

- If the verification succeeds, P_1, P_2, P_3 converts $\langle u \rangle$ to $\llbracket u \rrbracket$ locally by setting $m_u = 0$ and $\langle \lambda_u \rangle = -\langle u \rangle$.

Online: Let v denotes the bit m_b over ring \mathbb{Z}_{2^ℓ} .

- Parties execute $\Pi_{\text{vSh}}(P_1, P_2, P_3, v)$ to generate $\llbracket v \rrbracket$.
- Parties execute Π_{Mult} on $\llbracket u \rrbracket$ and $\llbracket v \rrbracket$ to generate $\llbracket uv \rrbracket$.
- Parties locally compute $\llbracket b \rrbracket = \llbracket v \rrbracket + \llbracket u \rrbracket - 2\llbracket uv \rrbracket$.

Figure 15: Bit to Arithmetic Sharing.

g) *Boolean to Arithmetic Sharing (B2A)*: We use the fact that a value v can be expressed as $\sum_{i=0}^{\ell-1} 2^i \cdot v_i$, where v_i denotes the i th bit of v over a ring \mathbb{Z}_{2^ℓ} . Note that

$$v = \sum_{i=0}^{\ell-1} 2^i \cdot v_i = \sum_{i=0}^{\ell-1} 2^i \cdot (m'_{v_i} + \lambda'_{u_i} - 2m'_{v_i} \cdot \lambda'_{u_i})$$

where m'_{v_i} and λ'_{u_i} denote the bits m_{v_i} and λ_{u_i} respectively over the ring \mathbb{Z}_{2^ℓ} .

The offline phase of Π_{B2A} proceeds similar to Π_{Bit2A} , where each bit λ_{v_i} for $i \in \{0, \dots, \ell-1\}$ is converted to $\langle \cdot \rangle$ -share. During the online phase, parties locally compute $\llbracket \cdot \rrbracket$ -shares of v followed by generating $\llbracket \cdot \rrbracket$ -shares of it by executing Π_{vSh} protocol. Parties then locally add their shares to obtain $\llbracket v \rrbracket$.

Protocol Π_{B2A}

Offline: Let v_i denotes the i th bit of value v . Let p_i and $\lambda_{p_i,j}$ denote the bits λ_{v_i} and $\lambda_{v_i,j}$ respectively over the ring \mathbb{Z}_{2^ℓ} , where $i \in \{0, \dots, \ell-1\}$ and $j \in \{1, 2, 3\}$.

- Parties execute offline steps of Π_{Bit2A} on each p_i for $i \in \{0, \dots, \ell-1\}$, to generate $\langle p_i \rangle$. Let the shares be $\langle p_i \rangle_{P_1} = (p_{i,2}, p_{i,3})$, $\langle p_i \rangle_{P_2} = (p_{i,3}, p_{i,1})$, and $\langle p_i \rangle_{P_3} = (p_{i,1}, p_{i,2})$.

Online: Let q_i for $i \in \{0, \dots, \ell-1\}$ denotes bit m_{v_i} over \mathbb{Z}_{2^ℓ} .

- Parties compute the following:
 - P_1, P_3 compute $x = \sum_{i=0}^{\ell-1} 2^i (q_i + p_{i,2} - 2q_i \cdot p_{i,2})$.
 - P_2, P_1 compute $y = \sum_{i=0}^{\ell-1} 2^i (p_{i,3} - 2q_i \cdot p_{i,3})$.
 - P_3, P_2 compute $z = \sum_{i=0}^{\ell-1} 2^i (p_{i,1} - 2q_i \cdot p_{i,1})$.
- Parties generate $\llbracket x \rrbracket, \llbracket y \rrbracket$ and $\llbracket z \rrbracket$ by executing $\Pi_{\text{vSh}}(P_1, P_3, x)$, $\Pi_{\text{vSh}}(P_2, P_1, y)$ and $\Pi_{\text{vSh}}(P_3, P_2, z)$ respectively.
- Parties locally compute $\llbracket v \rrbracket = \llbracket x \rrbracket + \llbracket y \rrbracket + \llbracket z \rrbracket$.

Figure 16: Boolean to Arithmetic Sharing V2.

h) *Bit Injection (BitInj)*: $\llbracket b \rrbracket^B \llbracket v \rrbracket \rightarrow \llbracket bv \rrbracket$: Let y_1 and y_2 denote the values λ_b and $\lambda_b \lambda_v$ respectively over ring \mathbb{Z}_{2^ℓ} . Similarly, let x_0, x_1, x_2 and x_3 denote the values $(m_b m_v)$, (m_b) , $(m_v - 2m_v m_b)$ and $(2m_b - 1)$ respectively over ring \mathbb{Z}_{2^ℓ} . Then,

$$b \cdot v = (m_b \oplus \lambda_b)(m_v - \lambda_v) = x_0 - x_1 y_1 + x_2 y_2 + x_3 y_3$$

Protocol Π_{BitInj}

Offline: Let y_1 and y_2 denote the values λ_b and $\lambda_b \lambda_v$ respectively over \mathbb{Z}_{2^ℓ} .

- P_0 executes $\Pi_{\text{aSh}}(P_0, y_j)$ to generate $\langle y_j \rangle$ for $j \in \{1, 2\}$. Let the shares be $\langle y_j \rangle_{P_1} = (y_{j,2}, y_{j,3})$, $\langle y_j \rangle_{P_2} = (y_{j,3}, y_{j,1})$, and

$$\langle y_j \rangle_{P_3} = (y_{j,1}, y_{j,2}).$$

- Parties verify the correctness of $\langle y_1 \rangle$ using the steps similar to protocol Π_{Bit2A} (Fig. 15). To verify the correctness of $\langle y_2 \rangle$, parties proceed as follows:
 - Parties execute Π_{Zero} (Fig. 21) to generate A, B, Γ such that $A + B + \Gamma = 0$.
 - P_1 computes $u_2 = \lambda_{y_1,2}\lambda_{v,2} + \lambda_{y_1,2}\lambda_{v,3} + \lambda_{y_1,3}\lambda_{v,2} + A$.
 - P_2 computes $u_3 = \lambda_{y_1,3}\lambda_{v,3} + \lambda_{y_1,3}\lambda_{v,1} + \lambda_{y_1,1}\lambda_{v,3} + B$.
 - P_3 computes $u_1 = \lambda_{y_1,1}\lambda_{v,1} + \lambda_{y_1,1}\lambda_{v,2} + \lambda_{y_1,2}\lambda_{v,1} + \Gamma$.
 - P_1 and P_2 send z_2 and $H(-z_3)$ respectively to P_3 , where $z_2 = u_2 - y_{2,2}$ and $z_3 = u_3 - y_{2,3}$.
 - P_3 sets $z_1 = u_1 - y_{2,1}$ and **abort** if $H(z_1 + z_2) \neq H(-z_3)$.

Online: Let x_0, x_1, x_2 and x_3 denote the values $(m_b m_v)$, (m_b) , $(m_v - 2m_v m_b)$ and $(2m_b - 1)$ respectively over ring \mathbb{Z}_{2^ℓ} .

- Parties compute the following:
 - P_1, P_3 compute $c_2 = x_0 - x_1 \lambda_{v,2} + x_2 y_{1,2} + x_3 y_{2,2}$.
 - P_2, P_1 compute $c_3 = -x_1 \lambda_{v,3} + x_2 y_{1,3} + x_3 y_{2,3}$.
 - P_3, P_2 compute $c_1 = -x_1 \lambda_{v,1} + x_2 y_{1,1} + x_3 y_{2,1}$.
- Parties execute $\Pi_{\text{vSh}}(P_1, P_3, c_2)$, $\Pi_{\text{vSh}}(P_2, P_1, c_3)$ and $\Pi_{\text{vSh}}(P_3, P_2, c_1)$ to generate $\llbracket c_2 \rrbracket$, $\llbracket c_3 \rrbracket$ and $\llbracket c_1 \rrbracket$ respectively.
- Parties locally compute $\llbracket bv \rrbracket = \llbracket c_1 \rrbracket + \llbracket c_2 \rrbracket + \llbracket c_3 \rrbracket$.

Figure 17: Bit Injection: $\llbracket b \rrbracket^B \llbracket v \rrbracket^A \rightarrow \llbracket bv \rrbracket^A$.

In the offline phase, P_0 generates $\langle \cdot \rangle$ -shares of λ'_b and $\lambda_b \lambda_v$ where λ'_b denotes the bit λ_b over \mathbb{Z}_{2^ℓ} . The check for $\langle \lambda'_b \rangle$ is the same as the one for Π_{Bit2A} , to check $\langle \lambda_b \lambda_v \rangle$ parties proceed as mentioned in protocol Π_{BitInj} above. During the online phase, parties locally compute $\llbracket \cdot \rrbracket$ -shares of $b \cdot v$ followed by generating $\llbracket \cdot \rrbracket$ -shares of it by executing Π_{vSh} protocol. Parties then locally add their shares to obtain $\llbracket b \cdot v \rrbracket$.

V. PRIVACY PRESERVING MACHINE LEARNING

Most of the intermediate values in machine learning algorithms involve operating over decimals. To represent decimal values, we use signed two's complement over \mathbb{Z}_{2^ℓ} [3], [6], [26], where the most significant bit (msb) represents the sign and the last d bits represent the fractional part.

In order to perform privacy-preserving machine learning, we need efficient instantiations of three components – Share Truncation, Secure Comparison, and Non-linear Activation Functions. This section covers our protocols for performing the aforementioned components.

A. Share Truncation

We take inspiration for truncation from ABY3 [6], where they perform it on shares after evaluating a multiplication gate, preserving the underlying value with very high probability. Our approach improves upon ABY3 by not using any boolean circuits, thus improving the offline round complexity to constant.

Protocol $\Pi_{\text{MultTr}}(w_x, w_y, w_z)$

Offline:

- Parties execute the offline steps of protocol $\Pi_{\text{Mult}}(w_x, w_y, w_z)$ apart from λ_z not being generated.
- Parties locally sample the following random values:

$$\mathcal{P} \setminus \{P_2\} : r_2, \quad \mathcal{P} \setminus \{P_1\} : r_1, \quad \mathcal{P} \setminus \{P_3\} : r_3$$

- P_0 locally compute $r = r_1 + r_2 + r_3$, locally truncates it to obtain r^t and executes $\Pi_{\text{aSh}}(P_0, r^t)$ to generate $\langle r^t \rangle$. Let the shares be $\langle r^t \rangle_{P_1} = (r_2^t, r_3^t)$, $\langle r^t \rangle_{P_2} = (r_3^t, r_1^t)$, and $\langle r^t \rangle_{P_3} = (r_1^t, r_2^t)$.
- Let r_d and $r_{d,i}$ denote the last d bits of r and r_i respectively for $i \in \{1, 2, 3\}$. Parties verify the correctness of $\langle r^t \rangle$ as follows:
 - P_1 samples a random element c and computes $m_1 = r_2 - 2^d r_2^t - r_{d,2} + c$. P_1 sends $(m_1, H(c))$ to P_2 .
 - P_2 computes $m_2 = (r_1 + r_3) - 2^d (r_1^t + r_3^t) - (r_{d,1} + r_{d,3})$ and **abort** if $H(m_1 + m_2) \neq H(c)$.
- Parties locally convert $\langle r^t \rangle$ to $\llbracket r^t \rrbracket$ by setting $m_{r^t} = 0$ and $\langle \lambda_{r^t} \rangle = \langle r^t \rangle$.

Online: Let $z' = (z - r) - m_x m_y$.

- Parties locally compute the following:
 - P_1, P_3 compute $\llbracket z' \rrbracket_2 = -\lambda_{x,2} m_y - \lambda_{y,2} m_x + \gamma_{xy,2} - r_2$.
 - P_2, P_1 compute $\llbracket z' \rrbracket_3 = -\lambda_{x,3} m_y - \lambda_{y,3} m_x + \gamma_{xy,3} - r_3$.
 - P_3, P_2 compute $\llbracket z' \rrbracket_1 = -\lambda_{x,1} m_y - \lambda_{y,1} m_x + \gamma_{xy,1} - r_1$.
- Parties exchange the following:
 - P_1 receives $\llbracket z' \rrbracket_1$ and $H(\llbracket z' \rrbracket_1)$ from P_2 and P_3 respectively.
 - P_2 receives $\llbracket z' \rrbracket_2$ and $H(\llbracket z' \rrbracket_2)$ from P_3 and P_1 respectively.
 - P_3 receives $\llbracket z' \rrbracket_3$ and $H(\llbracket z' \rrbracket_3)$ from P_1 and P_2 respectively.
- P_i for $i \in \{1, 2, 3\}$ **abort** if the received values are inconsistent. Else, he computes $(z - r) = \llbracket z' \rrbracket_1 + \llbracket z' \rrbracket_2 + \llbracket z' \rrbracket_3 + m_x m_y$.
- P_1, P_2, P_3 locally truncates $(z - r)$ to obtain $(z - r)^t$, followed by executing $\Pi_{\text{vSh}}(P_1, P_2, P_3, (z - r)^t)$ to generate $\llbracket (z - r)^t \rrbracket$.
- Parties locally compute $\llbracket z^t \rrbracket = \llbracket (z - r)^t \rrbracket + \llbracket r^t \rrbracket$.

Figure 18: Multiplication with Truncation.

We start by generating a random (r, r^t) in the offline phase, where r^t is the truncated value of r . The truncated value of z can be obtained by first opening and truncating $z - r$, and then adding it to r^t . Parties non-interactively generate $\langle r \rangle$, such that P_0 obtains r . This is followed by P_0 generating $\llbracket r^t \rrbracket$, but since we cannot rely on P_0 , parties P_1, P_2, P_3 perform a check to ensure the correctness of the share. On a high level, parties check the relation $r = 2^d r^t + r_d$, where r_d denotes the last d bits of r . The formal details of the check are provided in the protocol above and the correctness appears in Lemma D.1.

B. Secure Comparison

The secure comparison technique allows parties to check whether $x < y$, given arithmetic shares of x, y . In fixed point arithmetic, a simple way to achieve this is by computing $x - y$, and checking its sign, stored in the msb position. This protocol, inspired from [6], is called Bit Extraction (Π_{BitExt}), since it extracts a bit from the given arithmetic shares and outputs the boolean shares of the bit. Towards this, P_0, P_3 generate the boolean sharing of $y = \lambda_{v,1} + \lambda_{v,2}$ in the offline, while p_1, P_2 generate the boolean sharing of $x = m_v - \lambda_{v,3}$ in the online. Parties then evaluate the optimized Parallel Prefix Adder of [6] to obtain the MSB bit in boolean shared format.

C. Activation Functions

a) *ReLU*: The ReLU function is defined as $\text{relu}(v) = \max(0, v)$. This can be viewed as $\text{relu}(v) = (1 \oplus b)v$ where bit $b = 1$ if $v < 0$ and 0 otherwise. In order to generate $\llbracket \text{relu}(v) \rrbracket$, parties first execute Π_{BitExt} on v to obtain $\llbracket b \rrbracket^B$ and locally

compute $\llbracket 1 \oplus b \rrbracket^B$. This is followed by executing Π_{BitInj} on $\llbracket 1 \oplus b \rrbracket^B$ and $\llbracket v \rrbracket$. The derivative of relu , denoted by $\text{drelu}(v) = (1 \oplus b)$.

b) Sigmoid: In our protocols, we use the approximation of sigmoid function [3], [6], [26], defined as:

$$\text{sig}(v) = \begin{cases} 0 & v < -\frac{1}{2} \\ v + \frac{1}{2} & -\frac{1}{2} \leq v \leq \frac{1}{2} \\ 1 & v > \frac{1}{2} \end{cases}$$

This can be viewed as, $\text{sig}(v) = (1 \oplus b_1)b_2(v + 1/2) + (1 \oplus b_2)$, where $b_1 = 1$ if $v + 1/2 < 0$ and $b_2 = 1$ if $v - 1/2 < 0$. The protocol is similar to that of relu apart from an additional bit extraction, bit multiplication and a bit injection is required.

VI. IMPLEMENTATION AND BENCHMARKING

The improvements of our framework over the current state-of-the-art (ABY3) are showcased through our implementation, comparing the two. The training and prediction phases of Linear Regression, Logistic Regression, Neural Networks (NN), and Convolutional Neural Networks (CNN) are used for benchmarking. While we compare our construction with the malicious ABY3 in this section, the comparison of ours with the semi-honest version of ABY3 and with the 4PC protocol of Gordon et al. [27] are deferred to Appendix E.

a) Environment Details: We provide results for both LAN (1Gbps bandwidth) and WAN (40Mbps bandwidth) settings. In the LAN setting, we have machines with 3.6 GHz Intel Core i7-7700 CPU and 32 GB RAM. In the WAN setting, we use Google Cloud Platform² with machines located in West Europe (P_0), East Australia (P_1), South Asia (P_2) and South East Asia (P_3). We use n1-standard-8 instances, where machines are equipped with 2.3 GHz Intel Xeon E5 v3 (Haswell) processors supporting hyper-threading, with 8 vCPUs, and 30 GB RAM. We measured the average round-trip time (rtt) for communicating 1 KB of data between every pair of parties. Over the LAN setting, the rtt turned out to be 0.296ms. In the WAN setting, the rtt values were

P_0-P_1	P_0-P_2	P_0-P_3	P_1-P_2	P_1-P_3	P_2-P_3
274.83ms	174.13ms	219.45ms	152.3ms	60.19ms	92.63ms

We implement our protocols using the ENCRYPTO library [51] in C++17 over a 64 bit ring. Since the codes for ABY3 and MRZ [17] are not publicly available, we implement their protocols in our environment. The hash function is instantiated using SHA-256. We use multi-threading, wherever possible, to facilitate efficient computation and communication among the parties. To even out the results, each experiment is run 20 times and the average values are reported.

b) Datasets: To benchmark the training phase of machine learning algorithms, it is common practice to use synthetic datasets so that we have freedom to choose the parameters of the datasets. However, testing the accuracy of a trained model must be carried out with a real dataset, which is the MNIST [44] in our case. It contains 28×28 pixel images of handwritten numbers and 784 features. For benchmarking of the prediction phase, we use the following real-world datasets:

Dataset	#features	#samples
Candy (CD) Power Ranking [52]	13	85
Boston (BT) Housing Prices [53]	14	506
Weather (WR) Conditions in World War Two [54]	31	≈ 119000
CalCOFI (CI) - Oceanographic Data [55]	74	≈ 876000
Epileptic (EP) Seizures [56]	179	≈ 11500
Food Recipes (RE) [57]	680	≈ 20000
MNIST [44]	784	70000

We choose Boston, Weather and CalCOFI for linear regression, since they are best suited for it, while Candy, Epileptic and Recipes were chosen for logistic regression. For NN and CNN, we used the MNIST dataset.

A. Secure Training

The training phase in most of the machine learning algorithms consists of two stages– i) forward propagation, where the model computes the output, and ii) backward propagation, where the model parameters are adjusted according to the computed output and the actual output. We define one *iteration* in the training phase as one forward propagation followed by a backward propagation.

This section covers the improvements in the training phase of our protocol as compared to ABY3. We report the performance in terms of the number of iterations over varying feature (d) and batch sizes (B), where $d \in \{10, 100, 1000\}$ and $B \in \{128, 256, 512\}$. In LAN, we use *iterations per second* (#it/sec) as the metric, but since rtt is much higher for WAN, we instead use *iterations per minute* (#it/min).

a) Linear Regression: For linear regression, one iteration can be viewed as updating the weight vector \vec{w} using the Gradient Descent algorithm (GD). The update function for \vec{w} is given by

$$\vec{w} = \vec{w} - \frac{\alpha}{B} \mathbf{X}_i^T \circ (\mathbf{X}_i \circ \vec{w} - \mathbf{Y}_i)$$

where α denotes the learning rate and \mathbf{X}_i denotes a subset of batch size B , randomly selected from the entire dataset in the i th iteration. Here the forward propagation consists of computing $\mathbf{X}_i \circ \vec{w}$, while the weight vector is updated in the backward propagation. The update function consists of a series of matrix multiplications, which in turn can be achieved using dot product protocols. The operations of subtraction as well as multiplication by a public constant can be performed locally. We observe that the aforementioned update function can be computed entirely in the arithmetic domain and can be viewed in form of $\llbracket \cdot \rrbracket$ -shares as

$$\llbracket \vec{w} \rrbracket = \llbracket \vec{w} \rrbracket - \frac{\alpha}{B} \llbracket \mathbf{X}_j^T \rrbracket \circ (\llbracket \mathbf{X}_j \rrbracket \circ \llbracket \vec{w} \rrbracket - \llbracket \mathbf{Y}_j \rrbracket)$$

Table IV provides concrete values for Linear Regression. Our improvement over LAN ranges from $4.88 \times$ to $251.84 \times$ and $2 \times$ to $2.83 \times$ over WAN. The gain comes due to two factors: One being the amount we save through our feature-independent communication of the dot product protocol (3 ring elements as opposed to $9d$). The other factor is our efficient truncation protocol, which reduces the online communication from 12 elements to 3 elements – by 75%. The reason for the discrepancy in gains in LAN and WAN is because in LAN, the rtt is in the order of microseconds, and scales with the communication size. In contrast, the rtt in WAN is in the order of milliseconds and does not scale with communication up to a threshold, within which all our protocols operate.

²<https://cloud.google.com/>

Network	#features	Ref.	Batch Size B		
			128	256	512
LAN #it/sec	10	ABY3 This	287.36 1639.35	246.92 1204.82	186.22 1162.8
	100	ABY3 This	110.38 1587.31	62.08 1176.48	29.27 1136.37
	1000	ABY3 This	13.51 1095.3	6.88 883.4	3.43 861.33
WAN #it/min	10	ABY3 This	97.57 195.14	97.57 195.14	97.57 195.14
	100	ABY3 This	97.57 195.14	97.11 195.14	95.08 195.14
	1000	ABY3 This	89.95 195.14	80.10 195.14	68.94 195.14

Table IV: Comparison of ABY3 (Malicious) and **This** for Linear Regression (higher = better).

b) Logistic Regression: The iteration for the case of logistic regression is similar to that of linear regression, apart from an activation function being applied on $\mathbf{X}_i \circ \vec{\mathbf{w}}$ in the forward propagation. We instantiate the activation function using sigmoid (Section V-C). The update function for $\vec{\mathbf{w}}$ is given by

$$\vec{\mathbf{w}} = \vec{\mathbf{w}} - \frac{\alpha}{B} \mathbf{X}_i^T \circ (\text{sig}(\mathbf{X}_i \circ \vec{\mathbf{w}}) - \mathbf{Y}_i)$$

One iteration of logistic regression incurs an additional cost for computing $\text{sig}(\mathbf{X}_j \circ \vec{\mathbf{w}})$ as compared with that for linear regression.

Network	#features	Ref.	Batch Size B		
			128	256	512
LAN #it/sec	10	ABY3 This	56.95 338.99	42.02 257.01	30.35 226.61
	100	ABY3 This	43.34 336.71	27.89 255.69	16.2 225.64
	1000	ABY3 This	11.36 307.41	6.06 238.44	3.13 212.23
WAN #it/min	10	ABY3 This	20.54 55.76	20.54 55.76	20.52 55.76
	100	ABY3 This	20.54 55.76	20.52 55.76	20.41 55.76
	1000	ABY3 This	20.18 55.76	19.64 55.76	18.87 55.76

Table V: Comparison of ABY3 (Malicious) and **This** for Logistic Regression (higher = better).

Table V provides concrete values for Logistic Regression. Logistic Regression can be thought of as an execution of Linear Regression followed by a Sigmoid function on the output, due to which our improvements for Linear Regression carry over to Logistic. Our improvement ranges from $5.95\times$ to $67.88\times$ over LAN and $2.71\times$ to $2.96\times$ over WAN. Our efficient Sigmoid protocol takes this result further by improving upon the round and communication complexity. The round complexity is brought down to constant from $4 + \log \ell$ to 5. Instantiated over ring $\mathbb{Z}_{2^{64}}$, this amounts to an improvement of

50%. The communication is also improved by $\approx 80\%$ (from 81 elements to roughly 16).

c) Neural Networks: For the case of NN, we follow steps similar to that of ABY3, where each node across all the layers, except the last layer, uses ReLU (relu) as the activation function. At the output layer, we use the MPC friendly variant of the softmax activation function, $\text{smx}(\mathbf{u}_i) = \frac{\text{relu}(\mathbf{u}_i)}{\sum_{j=1}^n \text{relu}(\mathbf{u}_j)}$, proposed by SecureML [3]. In order to perform the division, we switch from arithmetic to garbled world and then use a division garbled circuit.

The network is trained using the Gradient Descent, where the forward propagation comprises of computing activation matrices for all the layers in the network. Here, the activation matrix for all the layers except the output, is defined as $\mathbf{A}_i = \text{relu}(\mathbf{U}_i)$, where $\mathbf{U}_i = \mathbf{A}_{i-1} \circ \mathbf{W}_i$. \mathbf{A}_0 is initialized to \mathbf{X}_j , where \mathbf{X}_j is a subset of batch size B , randomly selected from the entire dataset for the j^{th} iteration. The activation matrix for the output layer is defined as $\mathbf{A}_m = \text{smx}(\mathbf{U}_m)$.

During the backward propagation, error matrices are computed first. The error matrix for the output layer is defined as $\mathbf{E}_m = (\mathbf{A}_m - \mathbf{T})$, while for the remaining layers it is defined as $\mathbf{E}_i = (\mathbf{E}_{i+1} \circ \mathbf{W}_i^T) \otimes \text{drelu}(\mathbf{U}_i)$. Here the operation \otimes denotes element wise multiplication and drelu denotes the derivative of ReLU. This is followed by updating the weights as $\mathbf{W}_i = \mathbf{W}_i - \frac{\alpha}{B} \mathbf{A}_{i-1}^T \circ \mathbf{E}_i$.

Network	Ref.	LAN (#it/sec)			WAN (#it/min)		
		B-128	B-256	B-512	B-128	B-256	B-512
NN	ABY3	0.37	0.25	0.15	4.69	4.28	3.87
	This	23.00	13.55	7.70	13.94	13.94	13.79
CNN	ABY3	0.23	0.18	0.13	4.34	4.05	3.71
	This	10.46	5.63	2.99	13.86	13.67	13.16

Table VI: Comparison of ABY3 (Malicious) and **This** for NN and CNN (higher = better).

We consider a NN with two hidden layers, each having 128 nodes followed by an output layer of 10 nodes. After each layer, the activation function ReLU is applied. NN training involves one forward pass followed by one back-propagation. In LAN, the number of iterations is maximum with a batch of 128, at 22.99 #it/sec, and comes down to 7.70 with a batch size of 512. Similarly, over WAN it is maximum at 13.94 and comes down to 13.79. As expected, the #it/sec has not decreased with increase in features due to our dot product protocol being feature-independent in terms of communication. ABY3 on the other hand, has reported 2.5 #it/sec with a batch size of 128, in the computationally lighter semi-honest setting. Table VI above provides more details.

We also considered a CNN discussed in [5] with 2 hidden layers, consisting of 100 and 10 nodes. Similar to ABY3, we *overestimate* the running time by replacing the convolutional kernel with a fully connected layer. In LAN, the number of iterations is maximum with a batch of 128, at 10.46 #it/sec, and comes down to 2.99 with a batch size of 512. Similarly, over WAN it is maximum at 13.86 and comes down to 13.16.

B. Secure Prediction

For Secure Prediction, we use online latency of the protocol as a metric to compare both works. The units are milliseconds in LAN and seconds in WAN. We use the MNIST dataset, which has 784 features, with a batch size of 1 and 100 for benchmarking. Our truncation protocol causes a bit-error at the least significant bit position, which is the same as that of ABY3 and SecureML [3] due to similarity in the techniques. We refer the readers to SecureML for a detailed analysis of the bit-error. The accuracy of the prediction itself however, ranges from 93% for linear regression to 98.3% for CNN.

Network	Batch Size	Ref.	Linear Regression	Logistic Regression	NN	CNN
LAN (ms)	1	ABY3	2.08	6.25	73.09	371.1
		This	0.25	1.75	4.51	5.4
	100	ABY3	37.80	49.68	1284.95	2010.06
		This	0.30	2.55	17.17	39.63
WAN (s)	1	ABY3	0.47	2.77	6.02	6.25
		This	0.16	0.93	2.31	2.31
	100	ABY3	0.49	2.79	7.04	7.5
		This	0.16	0.93	2.31	2.32

Table VII: Online Runtime of ABY3 (Malicious) and **This** for Secure Prediction of Linear, Logistic, NN, and CNN models for $d = 784$. (lower = better).

For Linear Regression, our improvement ranges from $3\times$ to $126\times$, considering LAN and WAN together. For Logistic Regression, our improvement ranges from $3\times$ to $19.48\times$, considering LAN and WAN together. In NN, we achieve an improvement ranging from $3.05\times$ to $74.85\times$. Similarly for CNN, the improvement ranges from $2.71\times$ to $68.82\times$.

Though not stated explicitly, our offline cost for linear regression is orders of magnitude more efficient as compared to ABY3. This improvement carries over for logistic regression, NN, and CNN networks as well. A large part of this improvement comes from the difference in the approaches to truncation. ABY3’s approach entails using Ripple Carry Adder (RCA) circuits, which consume 128 rounds. ABY3 has pointed out that this was the reason SecureML performed better in total time for a single prediction. Our approach on the other hand, does not use any such circuit, resulting in an improvement of $\approx 15\times$ in communication and $64\times$ in rounds.

Throughput Comparison: We use a different metric to better illustrate the impact of our efficiency in the case of secure prediction, which is online throughput. Online throughput over LAN is the number of predictions that can be made in a second, and over WAN it is the number of predictions per minute. We have a total of 32 threads over 4 CPU cores, wherein each thread can perform 100 queries simultaneously without reduction in performance. Table VIII provides the online throughput comparison of ABY3 and ours for secure prediction over real-world datasets in a LAN setting. The gains for linear regression range from $26.16\times$ to $145.18\times$ and from $5.69\times$ to $158.40\times$ for logistic regression. Similarly, we observed gains of $335.44\times$ and $598.44\times$ for NN and CNN respectively.

In WAN, even though our protocols are more communication efficient as compared to ABY3, we could not fully capitalize on it especially for Linear Regression and Logistic

Ref.	Linear Regression			Logistic Regression			NN	CNN
	BT	WR	CI	CD	EP	RE	MNIST	
ABY3	4.08	1.74	0.73	2.20	0.29	0.08	0.46	0.06
This	106.67	106.67	106.67	12.55	12.55	12.55	153.39	37.43

Table VIII: Online Throughput Comparison of ABY3 (Malicious) and **This** for Secure Prediction over LAN. (higher = better)

Regression. This is due to the limitation of being able to run only 32 CPU threads in parallel, which amounts to a lot of bandwidth not being utilized. This gap can be closed by introducing more CPU threads into our infrastructure. However, since we could not do this, in order to showcase the efficiency better, we limit the bandwidth and compute the gain in online throughput. As evident from the plot in Fig. 19, the gain increases as we limit the bandwidth. We observe that our protocols for Linear Regression and Logistic Regression achieve maximum bandwidth utilization at around 1.5 Mbps. On the other hand, NN and CNN utilize the entire bandwidth, even at 40 Mbps. So decreasing the bandwidth does have an effect on the throughput for NN and CNN.

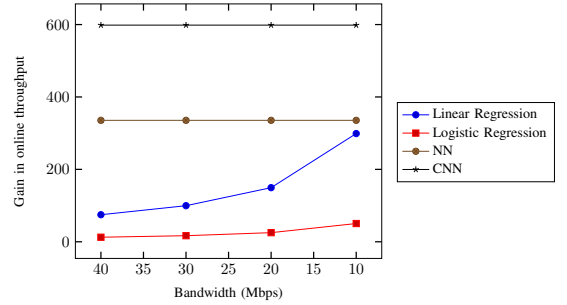


Figure 19: Throughput Gain in Low-end Networks.

VII. CONCLUSION

In this work, we presented an efficient privacy-preserving machine learning framework for the four-party setting, tolerating at most one malicious corruption. The theoretical improvements over the state-of-the-art 3PC framework of ABY3 were backed up by an extensive benchmarking. The improvements show that the availability of an additional honest party can improve the performance of ML protocols while at the same time, decreasing the total monetary cost of hiring the servers.

An interesting open problem is extending this framework to the n party setting. Improving the security to guaranteed output delivery with a minimal trade-off in the concrete performance is another challenging problem. Another direction would be to try to integrate the protocols proposed in this paper into a compiler such as HyCC [58], which already has support for the two-party framework of ABY.

Acknowledgements

Ajith Suresh would like to acknowledge financial support from Google PhD Fellowship 2019.

REFERENCES

- [1] N. Koti, A. Patra, R. Rachuri, and A. Suresh, "Tetrad: Actively Secure 4PC for Secure Training and Inference," *Cryptology ePrint Archive*, Report 2021/755, 2021, <https://eprint.iacr.org/2021/755>.
- [2] A. Madani, R. Arnaout, M. Mofrad, and R. Arnaout, "Fast and accurate classification of echocardiograms using deep learning," *CoRR*, 2017.
- [3] P. Mohassel and Y. Zhang, "SecureML: A system for scalable privacy-preserving machine learning," in *IEEE S&P*, 2017.
- [4] E. Makri, D. Rotaru, N. P. Smart, and F. Vercauteren, "EPIC: efficient private image classification (or: Learning from the masters)," *CT-RSA*, 2018.
- [5] M. S. Riaz, C. Weinert, O. Tkachenko, E. M. Songhori, T. Schneider, and F. Koushanfar, "Chameleon: A hybrid secure computation framework for machine learning applications," in *AsiaCCS*, 2018.
- [6] P. Mohassel and P. Rindal, "ABY³: A Mixed Protocol Framework for Machine Learning," in *ACM CCS*, 2018.
- [7] S. Wagh, D. Gupta, and N. Chandran, "SecureNN: Efficient and private neural network training," *IACR Cryptology ePrint Archive*, vol. 2018.
- [8] B. Pinkas, T. Schneider, N. P. Smart, and S. C. Williams, "Secure two-party computation is practical," in *ASIACRYPT*, 2009.
- [9] C. Orlandi, "Is multiparty computation any good in practice?" in *IEEE ICASSP*, 2011.
- [10] D. W. Archer, D. Bogdanov, Y. Lindell, L. Kamm, K. Nielsen, J. I. Pagter, N. P. Smart, and R. N. Wright, "From keys to databases - real-world applications of secure multi-party computation," *Comput. J.*, 2018.
- [11] T. Araki, J. Furukawa, Y. Lindell, A. Nof, and K. Ohara, "High-Throughput Semi-Honest Secure Three-Party Computation with an Honest Majority," in *ACM CCS*, 2016.
- [12] J. Furukawa, Y. Lindell, A. Nof, and O. Weinstein, "High-Throughput Secure Three-Party Computation for Malicious Adversaries and an Honest Majority," in *EUROCRYPT*, 2017.
- [13] T. Araki, A. Barak, J. Furukawa, T. Lichter, Y. Lindell, A. Nof, K. Ohara, A. Watzman, and O. Weinstein, "Optimized Honest-Majority MPC for Malicious Adversaries - Breaking the 1 Billion-Gate Per Second Barrier," in *IEEE S&P*, 2017.
- [14] Y. Lindell and A. Nof, "A Framework for Constructing Fast MPC over Arithmetic Circuits with Malicious Adversaries and an Honest-Majority," in *ACM CCS*, 2017.
- [15] K. Chida, D. Genkin, K. Hamada, D. Ikarashi, R. Kikuchi, Y. Lindell, and A. Nof, "Fast large-scale honest-majority MPC for malicious adversaries," in *CRYPTO*, 2018.
- [16] P. S. Nordholt and M. Veeningen, "Minimising Communication in Honest-Majority MPC by Batchwise Multiplication Verification," in *ACNS*, 2018.
- [17] P. Mohassel, M. Rosulek, and Y. Zhang, "Fast and Secure Three-party Computation: Garbled Circuit Approach," in *CCS*, 2015.
- [18] Y. Ishai, R. Kumaresan, E. Kushilevitz, and A. Paskin-Cherniavsky, "Secure computation with minimal interaction, revisited," in *CRYPTO*, 2015.
- [19] A. Patra and D. Ravi, "On the exact round complexity of secure three-party computation," *CRYPTO*, 2018.
- [20] M. Byali, A. Joseph, A. Patra, and D. Ravi, "Fast secure computation for small population over the internet," *ACM CCS*, 2018.
- [21] D. Bogdanov, R. Talviste, and J. Willemson, "Deploying Secure Multi-Party Computation for Financial Data Analysis," in *FC*, 2012.
- [22] J. Launchbury, D. Archer, T. DuBuisson, and E. Mertens, "Application-scale secure multiparty computation," in *ESOP*, 2014.
- [23] D. Bogdanov, L. Kamm, B. Kubo, R. Rebane, V. Sokk, and R. Talviste, "Students and taxes: a privacy-preserving social study using secure computation," *IACR Cryptology ePrint Archive*, 2015.
- [24] D. Bogdanov, S. Laur, and J. Willemson, "Sharemind: A framework for fast privacy-preserving computations," in *ESORICS*, 2008.
- [25] M. Geisler, "Viff: Virtual ideal functionality framework," 2007.
- [26] H. Chaudhari, A. Choudhury, A. Patra, and A. Suresh, "ASTRA: High-throughput 3PC over Rings with Application to Secure Prediction," in *IACR Cryptology ePrint Archive*, 2019.
- [27] S. D. Gordon, S. Ranellucci, and X. Wang, "Secure computation with low communication from cross-checking," in *ASIACRYPT*, 2018.
- [28] B. Pinkas, M. Rosulek, N. Trieu, and A. Yanai, "Spot-light: Lightweight private set intersection from sparse OT extension," in *CRYPTO*, 2019.
- [29] D. Beaver, "Efficient Multiparty Protocols Using Circuit Randomization," in *CRYPTO*, 1991.
- [30] D. Beaver, "Precomputing Oblivious Transfer," in *CRYPTO*, 1995.
- [31] Z. Beerliová-Trubíniová and M. Hirt, "Efficient Multi-party Computation with Dispute Control," in *TCC*, 2006.
- [32] Z. Beerliová-Trubíniová and M. Hirt, "Perfectly-Secure MPC with Linear Communication Complexity," in *TCC*, 2008.
- [33] E. Ben-Sasson, S. Fehr, and R. Ostrovsky, "Near-Linear Unconditionally-Secure Multiparty Computation with a Dishonest Minority," in *CRYPTO*, 2012.
- [34] A. Choudhury and A. Patra, "An Efficient Framework for Unconditionally Secure Multiparty Computation," *IEEE Trans. Information Theory*, 2017.
- [35] I. Damgård, V. Pastro, N. P. Smart, and S. Zakarias, "Multiparty Computation from Somewhat Homomorphic Encryption," in *CRYPTO*, 2012.
- [36] I. Damgård, M. Keller, E. Larraia, V. Pastro, P. Scholl, and N. P. Smart, "Practical covertly secure MPC for dishonest majority - or: Breaking the SPDZ limits," in *ESORICS*, 2013.
- [37] M. Keller, P. Scholl, and N. P. Smart, "An architecture for practical actively secure MPC with dishonest majority," in *ACM CCS*, 2013.
- [38] M. Keller, E. Orsini, and P. Scholl, "MASCOT: Faster Malicious Arithmetic Secure Computation with Oblivious Transfer," in *ACM CCS*, 2016.
- [39] C. Baum, I. Damgård, T. Toft, and R. W. Zakarias, "Better preprocessing for secure multiparty computation," in *ACNS*, 2016.
- [40] I. Damgård, C. Orlandi, and M. Simkin, "Yet another compiler for active security or: Efficient MPC over arbitrary rings," *CRYPTO*, 2018.
- [41] R. Cramer, I. Damgård, D. Escudero, P. Scholl, and C. Xing, "SPDZ2k: Efficient MPC mod 2^k for Dishonest Majority," *CRYPTO*, 2018.
- [42] M. Keller, V. Pastro, and D. Rotaru, "Overdrive: Making SPDZ great again," in *EUROCRYPT*, 2018.
- [43] W. Henecka, S. Kögl, A. Sadeghi, T. Schneider, and I. Wehrenberg, "TASTY: tool for automating secure two-party computations," in *ACM CCS*, 2010.
- [44] Y. LeCun and C. Cortes, "MNIST handwritten digit database," 2010. [Online]. Available: <http://yann.lecun.com/exdb/mnist/>
- [45] V. Kolesnikov and T. Schneider, "Improved Garbled Circuit: Free XOR Gates and Applications," in *ICALP*, 2008.
- [46] V. Kolesnikov, P. Mohassel, and M. Rosulek, "Flexor: Flexible garbling for XOR gates that beats free-xor," in *CRYPTO*, 2014.
- [47] S. Zahur, M. Rosulek, and D. Evans, "Two Halves Make a Whole - Reducing Data Transfer in Garbled Circuits Using Half Gates," in *EUROCRYPT*, 2015.
- [48] S. Gueron, Y. Lindell, A. Nof, and B. Pinkas, "Fast Garbling of Circuits Under Standard Assumptions," in *ACM CCS*, 2015.
- [49] M. Bellare, V. T. Hoang, S. Keelveedhi, and P. Rogaway, "Efficient Garbling from a Fixed-Key Blockcipher," in *IEEE S&P*, 2013.
- [50] M. Bellare, V. T. Hoang, and P. Rogaway, "Foundations of Garbled Circuits," in *ACM CCS*, 2012.
- [51] Cryptography and P. E. G. at TU Darmstadt, "ENCRYPTO Utils," https://github.com/encryptogroup/ENCRYPTO_utils, 2017.
- [52] W. Hickey, "The ultimate halloween candy power ranking," 2017. [Online]. Available: <https://www.kaggle.com/fivethirtyeight/the-ultimate-halloween-candy-power-ranking/>
- [53] D. Harrison and D. L. Rubinfield, "Hedonic housing prices and the demand for clean air," *Journal of Environmental Economics and Management*, 1978. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/0095069678900062>
- [54] NOAA, "Weather conditions in world war two," 2017. [Online]. Available: <https://www.kaggle.com/smid80/weatherww2/data>
- [55] S. Dane, "CalCOFI - over 60 years of oceanographic data," 2017. [Online]. Available: <https://www.kaggle.com/sohier/calcofi>

- [56] R. G. Andrzejak, K. Lehnertz, F. Mormann, C. Rieke, P. David, and C. Elger, "Indications of nonlinear deterministic and finite-dimensional structures in time series of brain electrical activity: Dependence on recording region and brain state," *Physical review. E, Statistical, nonlinear, and soft matter physics*, 2002.
- [57] H. Darwood, "Epicurious - recipes with rating and nutrition," 2017. [Online]. Available: <https://www.kaggle.com/hugodarwood/epirecipes/>
- [58] N. Büscher, D. Demmler, S. Katzenbeisser, D. Kretzmer, and T. Schneider, "Hycc: Compilation of hybrid protocols for practical secure computation," in *Proceedings of the 2018 ACM SIGSAC Conference on Computer and Communications Security, CCS 2018, Toronto, ON, Canada, October 15-19, 2018*, 2018.
- [59] V. A. Abril, P. Maene, N. Mertens, and N. P. Smart, "Bristol Fashion MPC Circuits," <https://homes.esat.kuleuven.be/~nsmart/MPC/>, 2019.

APPENDIX A BUILDING BLOCKS

a) Collision Resistant Hash: Consider a hash function family $H = \mathcal{K} \times \mathcal{L} \rightarrow \mathcal{Y}$. The hash function H is said to be collision resistant if for all probabilistic polynomial-time adversaries \mathcal{A} , given the description of H_k where $k \in_R \mathcal{K}$, there exists a negligible function $\text{negl}()$ such that $\Pr[(x_1, x_2) \leftarrow \mathcal{A}(k) : (x_1 \neq x_2) \wedge H_k(x_1) = H_k(x_2)] \leq \text{negl}(\kappa)$, where $m = \text{poly}(\kappa)$ and $x_1, x_2 \in_R \{0, 1\}^m$.

b) Shared Key Setup: Let $F : 0, 1^\kappa \times 0, 1^\kappa \rightarrow X$ be a secure PRF, with co-domain X being \mathbb{Z}_{2^ℓ} . The set of keys are:

- One key shared between every pair of parties - k_{ij} for (P_i, P_j) where $i, j \in \{0, 1, 2, 3\}$.
- One key shared between every group of three parties - k_{ijk} for (P_i, P_j, P_k) where $i, j, k \in \{0, 1, 2, 3\}$.
- One key shared amongst all - k_P .

We present the ideal world functionality $\mathcal{F}_{\text{setup}}$ below.

Functionality $\mathcal{F}_{\text{setup}}$

$\mathcal{F}_{\text{setup}}$ interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . $\mathcal{F}_{\text{setup}}$ picks random keys k_{ij} and k_{ijk} for $i, j, k \in \{0, 1, 2, 3\}$ and k_P . Let y_i denote the keys corresponding to party P_i . Then

- $y_i = (k_{01}, k_{02}, k_{03}, k_{012}, k_{013}, k_{023} \text{ and } k_P)$ when $P_i = P_0$.
- $y_i = (k_{01}, k_{12}, k_{13}, k_{012}, k_{013}, k_{123} \text{ and } k_P)$ when $P_i = P_1$.
- $y_i = (k_{02}, k_{12}, k_{23}, k_{012}, k_{023}, k_{123} \text{ and } k_P)$ when $P_i = P_2$.
- $y_i = (k_{03}, k_{13}, k_{23}, k_{013}, k_{023}, k_{123} \text{ and } k_P)$ when $P_i = P_3$.

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, y_i) to the adversary \mathcal{S} , where y_i denotes the keys corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, y_i).

Figure 20: Functionality for Shared Key Setup

c) Generating Zero Share, Π_{Zero} : Protocol Π_{Zero} (Fig. 21) enables parties in \mathcal{P} to generate a $\langle \cdot \rangle$ -sharing of zero among P_1, P_2, P_3 . In detail, parties P_1, P_2 and P_3 obtain values A, B and Γ respectively such that $A + B + \Gamma = 0$. In addition, P_0 obtains all the values A, B and Γ . The protocol is

adapted to the 4PC setting from the 3PC protocol of [11], and we use $\mathcal{F}_{\text{Zero}}$ to denote the ideal-world functionality for the same. We omit the proof for Π_{Zero} and refer readers to [11] since the protocols are almost similar.

Protocol Π_{Zero}

- Parties use the $\mathcal{F}_{\text{setup}}$ functionality to establish the following set of keys among them:

$$\mathcal{P} \setminus \{P_3\} : k_2, \quad \mathcal{P} \setminus \{P_1\} : k_3, \quad \mathcal{P} \setminus \{P_2\} : k_1$$

- Using the above set of keys and the PRF F , parties compute the following:
 - $P_0, P_1 : A = F(k_2) - F(k_1)$.
 - $P_0, P_2 : B = F(k_3) - F(k_2)$.
 - $P_0, P_3 : \Gamma = F(k_1) - F(k_3)$.

Figure 21: Generating $\langle \cdot \rangle$ -sharing of zero

APPENDIX B ANALYSIS OF OUR 4PC PROTOCOL

Lemma B.1 (Communication). *Protocol Π_{Sh} (Fig. 1) is non-interactive in the offline phase and requires 1 round and an amortized communication of 3ℓ bits in the online phase.*

Proof: During the offline phase, parties sample the λ -shares non-interactively using the shared key setup. During the online phase, P_i sends m-value to P_1, P_2, P_3 resulting in 1 round and a communication of at most 3ℓ bits (for the case when $P_i = P_0$). Parties P_1, P_2, P_3 then mutually exchange the hash of m-value received from P_0 . Parties can combine m-values for several instances into a single hash and hence the cost gets amortized over multiple instances. ■

Lemma B.2 (Communication). *Protocol Π_{aSh} (Fig. 2) requires 1 round and an amortized communication of 2ℓ bits in the offline phase.*

Proof: Protocol Π_{aSh} is performed entirely in the offline phase. During the protocol, P_0 computes and sends v_3 to both P_1 and P_2 resulting in 1 round and a communication of 2ℓ bits. Parties P_1, P_2 mutually exchange hash of the value received from P_0 to ensure consistency. Similar to Π_{Sh} , hash for multiple instances can be combined and hence this cost gets amortized over multiple instances. ■

Lemma B.3 (Communication). *Protocol Π_{Rec} (Fig. 3) requires 1 round and an amortized communication of 4ℓ bits in the online phase.*

Proof: During the protocol, each party receives his/her missing share from another party, resulting in 1 round and a communication of 4ℓ bits. In addition, each party receives a hash of the missing share from another party for verification. The hash for multiple instances can be combined to a single hash and thus this cost gets amortized over multiple instances. ■

Lemma B.4 (Communication). *Protocol Π_{Mult} (Fig. 4) requires 1 round and an amortized communication of 3ℓ bits*

in the offline phase and requires 1 round and an amortized communication of 3ℓ bits in the online phase.

Proof: During the offline phase, each P_1, P_2, P_3 receives one share of γ_{xy} from another party resulting in 1 round and communication of 3ℓ bits. Also, each party receives a hash value from P_0 in the same round. The values corresponding to all the multiplication gates can be combined into a single hash resulting in an overall communication of just three hash values, which gets amortized. During the online phase, each of P_1, P_2, P_3 receives one $[\cdot]$ -share of $m_z - m_x m_y$ from another party resulting in 1 round and a communication of 3ℓ bits. Also, each party receives a hash value of the same from the third party. The values corresponding to all the multiplication gates can be combined into a single hash resulting in amortization of this cost. ■

Theorem B.5. Π_{4PC} requires one round with an amortized communication of $3M$ ring elements during the offline phase. In the online phase, Π_{4PC} requires one round with an amortized communication of at most $3I$ ring elements in the Input-sharing stage, D rounds with an amortized communication of $3M$ ring elements for evaluation stage and one round with an amortized communication of $3O$ elements for the output-reconstruction stage.

Proof: The proof follows from Lemmas B.1, B.4 and B.3. ■

Lemma B.6 (Communication). Protocol Π_{fRec} (Fig. 5) requires 4 rounds and an amortized communication of 8ℓ bits in the online phase.

Proof: In the first round, P_0 receives a single bit from parties P_1, P_2, P_3 . In the next round, P_0 sends back a single bit to P_1, P_2, P_3 . In the third round, P_1, P_2, P_3 mutually exchanges the bit received from P_0 . In the last round, each party receives the missing share from two other parties resulting in the communication of 8ℓ bits. In parallel, each party receives a hash of the missing share from the third party. Note that the first three rounds can be performed only once, for all the output wires together, amortizing the corresponding cost. In the last round, parties can compute a single hash corresponding to all the output wires resulting in getting this cost amortized over all the instances. ■

APPENDIX C ANALYSIS OF CONVERSIONS

A. Building Blocks

Lemma C.1 (Communication). Protocol Π_{vSh} (Fig. 7) is non-interactive in the offline phase and requires 1 round and an amortized communication of at most 2ℓ bits in the online phase.

Proof: The cost for the offline phase follows from Lemma B.1. In the online phase, parties P_i, P_j sends the masked value to P_1, P_2, P_3 . For the case when $P_0 \in \{P_i, P_j\}$, this requires 1 round and a communication of 2ℓ bits. In other cases, communication of just ℓ bits is required. Note that the values corresponding to multiple instances of Π_{vSh} can be combined into a single hash, resulting in amortization of this cost. ■

Lemma C.2 (Communication). Protocol Π_{vSh}^G (Fig. 8) is non-interactive in the offline phase, while it requires 1 round and an amortised communication of κ bits in the online phase.

Proof: For the case when P_0 along with one of the garblers P_1, P_2, P_3 owns v , the protocol requires two commitments and one decommitment. ABY3 [6] has shown that the number of commitments can be limited to $2s$, when the number of values to be shared is larger than the statistical security parameter s . Consequently, the amortized cost per instance of Π_{vSh}^G becomes κ bits (where the garbler sends the key K_v^G to P_0). For the case when two of the garblers need to share multiple values, one garbler can combine all the actual keys to a single hash and send it to P_0 , resulting in an amortized cost of κ bits. ■

Lemma C.3 (Communication). Protocol Π_{DotP} (Fig. 9) requires 1 round and an amortized communication of 3ℓ bits in the offline phase and requires 1 round and an amortized communication of 3ℓ bits in the online phase.

Proof: The proof follows from Lemma B.4 since the protocol is similar to Π_{Mult} . ■

B. Sharing Conversions

We use $|Add|$ and $|Sub|$ to denote the size of garbled circuits corresponding to two ℓ -bit input adder and subtractor circuit respectively. We abuse the notation $|Decode|$ to denote the size of decoding information for the corresponding garbled circuit. The lemmas in this section provide the amortized cost and omit the cost of hash values.

Lemma C.4 (Communication). Protocol Π_{G2B} (Fig. 10) requires 1 round and a communication of $\kappa + 1 + |Decode|$ bits in the offline phase, while it requires 1 round and a communication of 3 bits in the online phase.

Proof: During the offline phase, parties execute one instance of Π_{vSh}^G and Π_{vSh}^B resulting in 1 round and a communication of $\kappa + 1$ bits (Lemmas C.1 and C.2). Also, the decoding information for performing an XOR in the garbled world needs to be communicated to P_0 . During the online phase, P_0 communicates a single bit to P_3 . Also, P_0 performs the boolean sharing of the same bit resulting in a total communication of 3 bits. The verification by P_3 can be pushed to the next round as long as the values are not revealed in the next round. Thus parties can proceed with the evaluation after the first round. ■

Lemma C.5 (Communication). Protocol Π_{G2A} (Fig. 11) requires 1 round and a communication of $\ell\kappa + \ell + |Sub| + |Decode|$ bits in the offline phase, while it requires 1 round and a communication of 3ℓ bits in the online phase.

Proof: The protocol is similar to that of Π_{G2B} (Lemma C.4) with the main difference being a garbled subtractor circuit Sub is used for evaluating the final output. ■

Lemma C.6 (Communication). Protocol Π_{B2G} (Fig. 12) requires 1 round and a communication of κ bits in the offline phase, while it requires 1 round and a communication of κ bits in the online phase.

Proof: The protocol Π_{B2G} involves the execution of an instance of protocol Π_{vSh}^G in both the offline and online phases. The cost follows directly from Lemma C.2. ■

Lemma C.7 (Communication). *Protocol Π_{A2G} (Fig. 13) requires ℓ rounds and a communication of $\ell\kappa + |\text{Sub}|$ bits in the offline phase, while it requires 1 round and a communication of $\ell\kappa$ bits in the online phase.*

Proof: The protocol can be viewed as ℓ instances of Π_{B2G} (Lemma C.6), where each bit of v is converted to its garbled sharing. Moreover, a garbled subtractor circuit Sub is used to evaluate the output. ■

Lemma C.8 (Communication). *Protocol Π_{A2B} (Fig. 14) requires 1 round and a communication of $3\ell \log \ell + 2\ell$ bits in the offline phase, while it requires $1 + \log \ell$ rounds and a communication of $3\ell \log \ell + \ell$ bits in the online phase.*

Proof: The protocol proceeds similarly to that of Π_{A2G} apart from the garbled world being replaced with the boolean world. Parties execute a single instance of Π_{vSh}^B in both the offline and online phases. This results in one round and a communication of 2ℓ bits in the offline phase, while it results in one round and communication of ℓ bits in the online phases (Lemma C.1). Moreover, parties evaluate a boolean subtractor circuit Sub (Parallel Prefix Adder version mentioned in ABY3 [6]) of $\log \ell$ multiplicative depth and contain $\ell \log \ell$ AND gates. This results in an additional communication of $3\ell \log \ell$ bits in the offline and online phases (Lemma B.4). Moreover, the online rounds increases from 1 to $1 + \log \ell$. ■

Lemma C.9 (Communication). *Protocol Π_{Bit2A} (Fig. 15) requires 2 rounds and a communication of $3\ell + 1$ bits in the offline phase, while it requires 1 round and a communication of 3ℓ bits in the online phase.*

Proof: During the offline phase, P_0 executes Π_{aSh} on u resulting in one round and a communication of 2ℓ bits (Lemma B.2). This is followed by parties P_1, P_2, P_3 performing the check to ensure the correctness of sharing done by P_0 . During the check, P_1 sends one ring element and a bit to P_3 , while P_2 sends a hash value. This results in an additional round and an amortized communication of $\ell + 1$ bits.

During the online phase, parties non-interactively generate the $[\cdot]_\ell$ -shares of v . This is followed by one arithmetic multiplication, resulting in one round and communication of 3ℓ bits in the online phase (Lemma B.4). Here, note that the offline phase for the multiplication is not required, since λ_v is set to 0 while executing the protocol $\Pi_{Sh}(P_1, P_2, P_3, v)$, resulting in γ_{uv} to be set to 0. ■

Lemma C.10 (Communication). *Protocol Π_{B2A} (Fig. 16) requires 2 rounds and a communication of $3\ell^2 + \ell$ bits in the offline phase, while it requires 1 round and a communication of 3ℓ bits in the online phase.*

Proof: The offline cost follows from that of protocol Π_{Bit2A} since the offline phase can be viewed as ℓ instances of that of Π_{Bit2A} (Lemma C.9). During the online phase, every pair from P_1, P_2, P_3 executes an instance of Π_{vSh} resulting in one round and communication of 3ℓ bits (Lemma C.1). ■

Lemma C.11 (Communication). *Protocol Π_{BitInj} (Fig. 17)*

requires 2 rounds and a communication of $6\ell + 1$ bits in the offline phase, while it requires 1 round and a communication of 3ℓ bits in the online phase.

Proof: The offline phase of Π_{BitInj} consists of generating $\langle \cdot \rangle$ -shares of λ_b and $\lambda_b \lambda_v$. The case for λ_b is same as that of Π_{Bit2A} resulting in two rounds and a communication of $3\ell + 1$ bits. In parallel, P_0 execute Π_{aSh} on $\lambda_b \lambda_v$ resulting in an additional communication of 2ℓ bits. In order to verify the correctness of this sharing, P_1, P_2, P_3 performs a check. During the check, P_1 sends one ring element to P_3 , while P_2 sends a hash value, resulting in an additional amortised communication of ℓ bits. Thus the offline phase requires two rounds and an amortised communication of $6\ell + 1$ bits.

During the online phase, every pair from P_1, P_2, P_3 executes an instance of Π_{vSh} resulting in one round and communication of 3ℓ bits (Lemma C.1). ■

Cost Comparison: Table IX provides a comparison of our sharing conversions with ABY3 [6]. Here, $G1$ denotes a two input garbled subtractor circuit, while $G2 = \text{Gar}(2, \text{Sub}, \ell)$ denotes two input garbled subtractor circuit along with its decoding information. Similarly, $G3 = \text{Gar}(3, \text{Sub}, \ell)$ and $G4 = \text{Gar}(3, \text{Adder}, \ell)$ denote three input garbled circuit for subtraction and addition respectively. Here ℓ denotes the ring size in bits.

Conv.	Work	Offline		Online	
		R.	Comm.	R.	Comm.
G2B	ABY3 This	1 1	κ $\kappa + 1 + \text{Decode} $	1 1	κ 3
G2A	ABY3 This	1 1	$ G3 + \ell\kappa$ $\ell\kappa + \ell + G2 $	1 1	$2\ell\kappa$ 3ℓ
B2G	ABY3 This	0 1	0 κ	1 1	2κ κ
A2G	ABY3 This	1 1	$ G4 $ $\ell\kappa + G1 $	1 1	$2\ell\kappa$ $\ell\kappa$
A2B	ABY3 This	3 1	$12\ell \log \ell + 12\ell$ $3\ell \log \ell + 2\ell$	$1 + \log \ell$ $1 + \log \ell$	$9\ell \log \ell + 9\ell$ $3\ell \log \ell + \ell$
Bit2A	ABY3 This	1 2	24ℓ $3\ell + 1$	2 1	18ℓ 3ℓ
B2A	ABY3 This	3 2	$12\ell \log \ell + 12\ell$ $3\ell^2 + \ell$	$1 + \log \ell$ 1	$9\ell \log \ell + 9\ell$ 3ℓ
BitInj	ABY3 This	1 2	36ℓ $6\ell + 1$	3 1	27ℓ 3ℓ

Table IX: Sharing conversions of ABY3 and Ours.

APPENDIX D ANALYSIS OF ML PROTOCOLS

Lemma D.1 (Correctness). *In the offline phase of protocol Π_{MultTr} (Fig. 18), if a corrupt P_0 generates incorrect $[\mathbf{r}^t]$ sharing, then the honest evaluators P_1, P_2, P_3 abort.*

Proof: To see the correctness, it suffices to show that $m_1 + m_2 = c$ where $m_1 = r_2 - 2^d r_2^t - r_{d,2} + c$ and $m_2 = (r_1 + r_3) - 2^d(r_1^t + r_3^t) - (r_{d,1} + r_{d,3})$. Note that $r = 2^d r^t + r_d$ where r^t denoted the truncated value of r and r_d denoted the last d bits

of r . Then,

$$\begin{aligned}
m_1 + m_2 &= (r_2 - 2^d r_2^t - r_{d,2} + c) \\
&\quad + ((r_1 + r_3) - 2^d(r_1^t + r_3^t) - (r_{d,1} + r_{d,3})) \\
&= (r_1 + r_2 + r_3) - 2^d(r_1^t + r_2^t + r_3^t) \\
&\quad - (r_{d,1} + r_{d,2} + r_{d,3}) + c \\
&= (r) - (2^d r^t + r_d) + c = 0 + c = c
\end{aligned}$$

Lemma D.2 (Communication). *Protocol Π_{MultTr} (Fig. 18) requires 2 rounds and a communication of 6ℓ bits in the offline phase, while it requires 1 round and a communication of 3ℓ bits in the online phase.*

Proof: During the offline phase, the offline phase of Π_{Mult} is executed, resulting in one round and communication of 3ℓ bits. In parallel, P_0 executes Π_{aSh} on r^t resulting in an additional communication of 2ℓ bits (Lemma B.2). To verify the correctness of this sharing, parties P_1, P_2, P_3 performs a check, where P_1 sends one ring element and hash value to P_2 . This results in an additional amortized communication of ℓ bits. Thus the offline phase requires two rounds and an amortized communication of 6ℓ bits. The cost for online phase follows directly from Π_{Mult} (Lemma B.4) ■

Lemma D.3 (Communication). *Protocol Π_{BitExt} (Fig. ??) requires 1 round and a communication of $4\ell + 1$ bits in the offline phase, while it requires 3 rounds and a communication of $5\ell + 2$ bits in the online phase.*

Proof: During the offline phase, parties execute one instance each of Π_{vSh} and Π_{vSh}^B resulting in one round and a communication of $\ell + 1$ bits (Lemma C.1). Also, the offline phase for multiplication is performed resulting in an additional communication of 3ℓ bits.

During the online phase, parties first execute an arithmetic multiplication, resulting in one round and communication of 3ℓ bits (Lemma B.4). The value rv is reconstructed towards both P_0 and P_3 , resulting in an additional round and an amortized communication of 2ℓ bits. This is followed by the last round, where parties execute one instance of Π_{vSh}^B resulting in a communication of 2 bits. Thus the online phase requires three rounds and an amortized communication of $5\ell + 2$ bits. ■

Lemma D.4 (Communication). *Protocol ReLU (Π_{relu}) requires 3 rounds and a communication of $8\ell + 2$ bits in the offline phase, while it requires 4 rounds and a communication of $8\ell + 2$ bits in the online phase.*

Proof: The cost follows directly from Lemmas D.3 and C.11. ■

Lemma D.5 (Communication). *Protocol Sigmoid (Π_{sig}) requires 3 rounds and a communication of $15\ell + 7$ bits in the offline phase, while it requires 5 rounds and a communication of $16\ell + 7$ bits in the online phase.*

Proof: The cost follows directly from Lemmas D.3, B.4, C.9 and C.11. ■

Cost Comparison: Table X provides a comparison of our work with ABY3 [6], in terms of ML protocols and special conversions.

Conv.	Work	Offline		Online	
		R.	Comm.	R.	Comm.
Π_{MultTr}	ABY3 This	$2\ell - 2$ 2	$96\ell - 42d - 84$ 6ℓ	1 1	12ℓ 3ℓ
Π_{BitExt}	ABY3 This	1 1	$24\ell \log \ell$ $4\ell + 1$	$\log \ell$ 3	$18\ell \log \ell$ $5\ell + 2$
ReLU	ABY3 This	3 3	60ℓ $8\ell + 2$	$3 + \log \ell$ 4	45ℓ $8\ell + 2$
Sigmoid	ABY3 This	3 3	$108\ell + 12$ $15\ell + 7$	$4 + \log \ell$ 5	$81\ell + 9$ $16\ell + 7$

Table X: ML conversions of ABY3 and Ours. d denotes the number of features

APPENDIX E BENCHMARKING

A. Motivating 4PC

In this section, we justify the reason for operating in the 4PC setting over the 3PC setting through the lens of benchmarking and practical efficiency. We begin with the comparison of our 4PC secret sharing scheme with that of Gordon et al. [27], which shows the reason we built a new 4PC. In Section E-A2 we compare the efficiency of our framework with ABY3. Since machine learning models take a long time to train, monetary cost becomes an important factor to consider when building PPML protocols, as shown in [28].

Monetary cost is computed by calculating the total running time for each of the servers and the cost of hiring them, which is based upon the computing power of the server. Total running time is the total time taken for the evaluation phase, which excludes the input sharing and reconstruction phase. So, if we have a protocol that communicates very less but the total running time for the servers is high due to the computation, the monetary cost will also be high. We compare the total running times of our protocol against the others to show that we have a better balance in terms of computation and communication, making our monetary cost lower than the rest.

Ref.	P_0	P_1	P_2	P_3	Total
Gordon This	7.84 0.00	3.13 6.19	7.34 6.19	3.21 3.81	21.52 16.19

Table XI: Total Online Runtime (in seconds) of Gordon et al. and This for evaluation on an AES-128 circuit (lower = better) over WAN.

1) *Comparison with the protocol of Gordon et al. [27]:* In Table XI, we compare the total online runtime of our 4PC protocol with that of Gordon et al. [27] for evaluating an AES-128 [59] circuit. As evident from the table, P_0 does not have to be active during the online phase, except for the sharing and the reconstruction phase. This means we can shut down the server that corresponds to P_0 for the entirety of the online evaluation phase, saving a lot in terms of monetary cost. The preprocessing phase for both protocols requires just 1 round of interaction.

2) *Motivation of 4PC for ML:* Here, we argue that even though we operate in the 4PC setting, meaning we have 4

servers active instead of 3 as in ABY3, our total monetary cost is still lower than that of ABY3. Table XII shows that our total runtime for both training and prediction phases in the malicious case is lower for all the algorithms considered. For the training phase, we use a batch size of 128. The number of features for both training and prediction is 784.

Phase	Ref.	Linear Regression	Logistic Regression	NN	CNN
Training (s)	ABY3 This	2.01 0.92	8.92 3.76	38.41 13.07	41.45 13.19
Prediction (s)	ABY3 This	1.45 0.44	8.36 2.74	21.12 6.90	22.48 6.93

Table XII: Total Online Runtime (in seconds) of ABY3 (Malicious) and **This** for Training and Prediction of Linear, Logistic, NN, and CNN models for $d = 784$ (lower = better) over a WAN setting.

B. Comparison with the ML framework of ABY3 [6] in the semi-honest setting

We compare the performance of our protocol with the semi-honest version of ABY3, giving them an advantage in terms of the threat model. We use ABY3S to denote the ABY3 protocol in the semi-honest setting. Our performance is the same for linear regression but as the protocols get more complex, the difference in performance increases in our favour. This is because in linear regression though we have the additional overhead of a hash, the cost is amortized, making our cost the same as ABY3S. For the other three algorithms, however, we outperform ABY3S due to our efficient protocols for bit extraction, bit injection, and Bit2A. Although we are worse than ABY3 in the offline phase due to our preprocessing for multiplication, our online phase is a lot more efficient for both training and prediction phases.

Algorithm	Ref.	LAN (#it/sec)	WAN (#it/min)
Linear Regression	ABY3S This	1098.90 1098.90	195.13 195.13
Logistic Regression	ABY3S This	90.29 307.41	35.48 55.75
Neural Networks	ABY3S This	1.01 23.00	8.13 13.94
CNN	ABY3S This	0.37 10.46	7.13 13.86

Table XIII: Comparison of Online Phase of ABY3 (Semi-Honest) and **This** for ML Training (higher = better)

In Table XIII, we compare the online phase of both protocols for ML training, in terms of the number of iterations per second they can process. In Table XIV and Table XV we compare the online phase for prediction through two benchmarking units, one being the runtime and the other being the throughput, which is the number of queries we can process per second.

Network	Ref.	Linear Regression	Logistic Regression	NN	CNN
LAN (ms)	ABY3S This	0.30 0.30	9.14 2.55	480.81 17.17	1185.70 39.63
WAN (s)	ABY3S This	0.16 0.16	1.54 0.93	4.07 2.31	4.47 2.31

Table XIV: Online Runtime of ABY3 (Semi-honest) and **This** for Secure Prediction of Linear, Logistic, NN, and CNN models for $d = 784$ (lower = better)

Algorithm	Ref.	LAN (queries/sec)	WAN (queries/min)
Linear Regression	ABY3S This	106666.67 106666.67	12488.62 12488.62
Logistic Regression	ABY3S This	3512.62 12549.02	1248.86 2081.46
Neural Networks	ABY3S This	66.41 153.39	211.18 368.13
CNN	ABY3S This	21.47 37.43	51.54 89.84

Table XV: Comparison of Online TP of ABY3 (Semi-Honest) and **This** for Secure Prediction (higher = better)

APPENDIX F SECURITY OF OUR CONSTRUCTIONS

In this section, we prove the security of our Π_{4PC} protocol in the $\{\mathcal{F}_{\text{setup}}, \mathcal{F}_{\text{Zero}}\}$ -hybrid model, using the real world-ideal world paradigm. The ideal world functionality that realises Π_{4PC} is given in Fig. 22.

Functionality \mathcal{F}_{4PC}

\mathcal{F}_{4PC} interacts with the parties in \mathcal{P} and the adversary \mathcal{S} and is parameterized by a 4-ary function f , represented by a publicly known arithmetic circuit ckt over \mathbb{Z}_{2^ℓ} .

Input: Upon receiving the input x_1, \dots, x_l from the respective parties in \mathcal{P} , do the following: if (Input, *) message was received from P_i corresponding to x_j , then ignore. Otherwise record $x'_j = x_j$ internally. If $x'_j \neq \mathbb{Z}_{2^\ell}$, consider $x'_j = \text{abort}$.

Output to adversary: If there exists $j \in \{1, \dots, l\}$ such that $x'_j = \text{abort}$, send (Output, \perp) to all the parties. Else, send (Output, (y_1, \dots, y_o)) to the adversary \mathcal{S} , where $(y_1, \dots, y_o) = f(x'_1, \dots, x'_l)$.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party belongs to I , send (Output, \perp), else send (Output, (y_1, \dots, y_o)).

Figure 22: Ideal world 4PC functionality

We begin with the case when P_0 is corrupted.

Theorem F.1. *In $\{\mathcal{F}_{\text{setup}}, \mathcal{F}_{\text{Zero}}\}$ -hybrid model, Π_{4PC} securely realizes the functionality \mathcal{F}_{4PC} against a static, malicious adversary \mathcal{A} , who corrupts P_0 .*

Proof: Let \mathcal{A} be a real-world adversary corrupting the

distributor P_0 during the protocol Π_{4PC} . We present an ideal-world adversary (simulator) \mathcal{S}_{4PC} for \mathcal{A} in Fig. 23 that simulates messages for corrupt P_0 . The only communication to P_0 is during the output-reconstruction stage in the online phase. \mathcal{S}_{4PC} can easily simulate these messages, with the knowledge of function output and the masks corresponding to the circuit-output wires.

Simulator \mathcal{S}_{4PC}

The simulator plays the role of the honest parties P_1, P_2, P_3 and simulates each step of Π_{4PC} to corrupt P_0 as follows and finally outputs \mathcal{A} 's output. The simulator initializes a boolean variable $\text{flag} = 0$, which indicates whether an honest party aborts during the protocol.

Offline Phase: \mathcal{S}_{4PC} emulates $\mathcal{F}_{\text{setup}}$ and gives the keys $(k_{01}, k_{02}, k_{03}, k_{012}, k_{013}, k_{023})$ and k_P to P_0 . By emulating $\mathcal{F}_{\text{setup}}$, it learns the λ -masks for all the wires in ckt.

- *Sharing Circuit-input Values:* Here the simulator has to do nothing, as the offline phase involves no communication.
- *Gate Evaluation:* No simulation is needed for the offline phase of an addition gate. For a multiplication gate g , the simulator emulates $\mathcal{F}_{\text{Zero}}$ and gives A, B, Γ to P_0 . It then receives hash of $\gamma_{xy,1}, \gamma_{xy,2}$ and $\gamma_{xy,3}$ from P_0 on behalf of P_2, P_3 and P_1 respectively. \mathcal{S}_{4PC} computes $\gamma_{xy,i}$ for $i \in \{1, 2, 3\}$ and sets $\text{flag} = 1$, if any of the hash values received is inconsistent with the values computed.
- *Output Reconstruction:* Here the simulator has to do nothing, as the offline phase involves no communication.

If $\text{flag} = 1$, \mathcal{S}_{4PC} invokes \mathcal{F}_{4PC} with input \perp on behalf of P_0 .

Online Phase:

- *Sharing Circuit-input Values:* For every input v_j of P_0 , \mathcal{S}_{4PC} receives m_{v_j} of behalf of P_1, P_2, P_3 . \mathcal{S}_{4PC} sets $\text{flag} = 1$ if the received values mismatch. Else, it computes the input v_j using m_{v_j} and the λ -masks obtained in the offline phase.
- *Gate Evaluation:* No simulation is needed for the online phase of both addition and multiplication gates.
- *Obtaining function outputs:* If $\text{flag} = 1$, \mathcal{S}_{4PC} invokes \mathcal{F}_{4PC} with input \perp on behalf of P_0 . Else it sends inputs $\{v_j\}$ extracted on behalf of P_0 to \mathcal{F}_{4PC} and receives the function outputs y_1, \dots, y_O .
- *Output Reconstruction:* For each output y_j for $j \in 1, \dots, O$, \mathcal{S}_{4PC} computes $m_{y_j} = y_j + \lambda_{y_j}$, and sends m_{y_j} and $H(m_{y_j})$ to P_0 on behalf of P_1 and P_2 respectively. In parallel, he receives $H(\lambda_{y_j,i})$ for $i \in \{1, 2, 3\}$ from P_0 , on behalf of P_1, P_2, P_3 . \mathcal{S}_{4PC} initializes the set I to \emptyset . P_i for $i \in \{1, 2, 3\}$, is added to I , if hash of $\lambda_{y_j,i}$ mismatches with the corresponding hash received from P_0 . \mathcal{S}_{4PC} then sends I to \mathcal{F}_{4PC} and terminates.

Figure 23: Simulator for the case of corrupt P_0

The proof now simply follows from the fact that simulated view and real-world view of the adversary are computationally indistinguishable. ■

We next consider the case, when the adversary corrupts one of the evaluators, say P_1 . The cases of corrupt P_2 and P_3 are handled symmetrically.

Theorem F.2. *In the $\{\mathcal{F}_{\text{setup}}, \mathcal{F}_{\text{Zero}}\}$ -hybrid model, Π_{4PC} securely realizes the functionality \mathcal{F}_{4PC} against a static, mali-*

cious adversary \mathcal{A} , who corrupts P_1 .

Proof: Let \mathcal{A} be a real-world adversary corrupting the evaluator P_1 during the protocol Π_{4PC} . We now present the steps of the ideal-world adversary (simulator) \mathcal{S}_{4PC} for \mathcal{A} for this case in Fig. 24.

Simulator \mathcal{S}_{4PC}

The simulator plays the role of the honest parties P_0, P_2, P_3 and simulates each step of Π_{4PC} to corrupt P_1 as follows and finally outputs \mathcal{A} 's output. The simulator initializes a boolean variable $\text{flag} = 0$, which indicates whether an honest party aborts during the protocol.

Offline Phase: \mathcal{S}_{4PC} emulates $\mathcal{F}_{\text{setup}}$ and gives the keys $(k_{01}, k_{12}, k_{13}, k_{012}, k_{013}, k_{123})$ and k_P to P_1 . By emulating $\mathcal{F}_{\text{setup}}$, it learns the λ -masks for all the wires in ckt.

- *Sharing Circuit-input Values:* Here the simulator has to do nothing, as the offline phase involves no communication.
- *Gate Evaluation:* No simulation is needed for the offline phase of an addition gate. For a multiplication gate $g = (w_x, w_y, w_z, \times)$, the simulator emulates $\mathcal{F}_{\text{Zero}}$ and gives A to P_1 . \mathcal{S}_{4PC} computes $\gamma_{xy,i}$ for $i \in \{1, 2, 3\}$ and sends $\gamma_{xy,3}$ and $H(\gamma_{xy,3})$ to P_1 on behalf of P_2 and P_0 respectively. It receives $\gamma_{xy,2}$ from P_1 on behalf of P_3 and sets $\text{flag} = 1$, if the received value is inconsistent.
- *Output Reconstruction:* Here the simulator has to do nothing, as the offline phase involves no communication.

If $\text{flag} = 1$, \mathcal{S}_{4PC} invokes \mathcal{F}_{4PC} with input \perp on behalf of P_0 .

Online Phase:

- *Sharing Circuit-input Values:* For every input v_j of P_1 , \mathcal{S}_{4PC} receives m_{v_j} of behalf of P_2 and P_3 . \mathcal{S}_{4PC} sets $\text{flag} = 1$ if the received values mismatch. Else, it computes the input v_j using m_{v_j} and the λ -masks obtained in the offline phase. For every input v_k of P_i for $i \in \{0, 2, 3\}$, \mathcal{S}_{4PC} sets $v_k = 0$ and sends $m_{v_k} = 0 + \lambda_{v_k}$ to P_1 on behalf of P_i . \mathcal{S}_{4PC} performs the exchange of hash of m_{v_k} honestly.
- *Gate Evaluation:* No simulation is needed for the online phase of addition gates. For a multiplication gate $g = (w_x, w_y, w_z, \times)$, \mathcal{S}_{4PC} computes and sends $[m'_{z1}]$ and $H([m'_{z1}])$ to P_1 on behalf of P_2 and P_3 respectively. It receives $H([m'_{z2}])$ and $[m'_{z3}]$ from P_1 on behalf of P_2 and P_3 respectively. \mathcal{S}_{4PC} sets $\text{flag} = 1$, if any of the received values are inconsistent.
- *Obtaining function outputs:* If $\text{flag} = 1$, \mathcal{S}_{4PC} invokes \mathcal{F}_{4PC} with input \perp on behalf of P_1 . Else it sends inputs $\{v_j\}$ extracted on behalf of P_1 to \mathcal{F}_{4PC} and receives the function outputs y_1, \dots, y_O .
- *Output Reconstruction:* For each output y_j for $j \in 1, \dots, O$, \mathcal{S}_{4PC} computes $\lambda_{y_j} = m_{y_j} - y_j$, and sends λ_{y_j} and $H(\lambda_{y_j})$ to P_1 on behalf of P_2 and P_0 respectively. In parallel, it receives $\lambda_{y_j,3}$ and $H(m_{y_j})$ from P_1 on behalf of P_3 and P_0 respectively. P_0 is added to I , if hash of m_{y_j} mismatches with the corresponding hash received from P_1 . Similarly, P_3 is added to I , if $\lambda_{y_j,3}$ mismatches with the corresponding value received from P_1 . \mathcal{S}_{4PC} then sends I to \mathcal{F}_{4PC} and terminates.

Figure 24: Simulator for the case of corrupt P_1

A. Security Proof in Detail

This section covers the security proofs for most of our constructions. The proofs for the rest can be easily derived. The proofs use the real-world/ideal-world paradigm in which \mathcal{A} is the adversary in the real-world and \mathcal{S} is the simulator for the ideal-world, which acts as the honest parties in the protocol and simulates messages received by \mathcal{A} . The simulator maintains a flag which is set to 0 at the start of the protocol. If an honest party aborts, the flag is set to 1. Simulator for a particular protocol is represented as \mathcal{S} with the protocol name as the subscript.

The simulation for a circuit ckt proceeds as follows: We start with the input sharing phase, and \mathcal{S} sets the input of the honest parties to 0. The simulator can extract the input of the \mathcal{A} from the sharing protocol Π_{Sh} , details of which are provided in the simulation for Π_{Sh} . Doing so gives the \mathcal{S} all the inputs for the entire circuit, which means it can compute all the intermediate values and the output of the circuit. As we will see later, \mathcal{S} will use this information to simulate each component of a circuit ckt.

For each of the constructions, we provide simulation proof for the case of corrupt P_0 and P_1 . The cases of corrupt P_2 and P_3 follows similar to that of P_1 .

a) *Sharing Protocol*: The ideal functionality realising protocol Π_{Sh} is presented in Fig. 25.

Functionality \mathcal{F}_{Sh}

\mathcal{F}_{Sh} interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . \mathcal{F}_{Sh} receives the input v from party P_i while it receives \perp from the other parties. If $v = \perp$, then send \perp to every party, else proceed with the computation.

Computation of output: Randomly select $\lambda_{v,1}, \lambda_{v,2}, \lambda_{v,3}$ from \mathbb{Z}_{2^ℓ} and set $m_v = v + \lambda_{v,1} + \lambda_{v,2} + \lambda_{v,3}$. The output shares are set as:

$$\begin{aligned} \llbracket v \rrbracket_{P_0} &= (\lambda_{v,1}, \lambda_{v,2}, \lambda_{v,3}) & \llbracket v \rrbracket_{P_1} &= (m_v, \lambda_{v,2}, \lambda_{v,3}) \\ \llbracket v \rrbracket_{P_2} &= (m_v, \lambda_{v,3}, \lambda_{v,1}) & \llbracket v \rrbracket_{P_3} &= (m_v, \lambda_{v,1}, \lambda_{v,2}) \end{aligned}$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket v \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket v \rrbracket_{\mathcal{S}}$ denotes the share of v corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket v \rrbracket_i$), where $\llbracket v \rrbracket_i$ denotes the share of v corresponding to the honest party P_i .

Figure 25: Functionality for protocol Π_{Sh}

The simulator for the case of corrupt P_0 appears in Fig. 26.

Simulator \mathcal{S}_{Sh}

Offline Phase: \mathcal{S}_{Sh} emulates $\mathcal{F}_{\text{setup}}$ and gives the keys ($k_{01}, k_{02}, k_{03}, k_{012}, k_{013}, k_{023}$ and k_P) to \mathcal{A} . By emulating $\mathcal{F}_{\text{setup}}$, it learns the λ -values corresponding to input v .

– If $P_i = P_0$, \mathcal{S}_{Sh} computes $\lambda_{v,j}$ for $j \in \{1, 2, 3\}$ on behalf of each

P_j using the shared key.

– If $P_i = P_k$ for $k \in \{1, 2, 3\}$, \mathcal{S}_{Sh} computes $\lambda_{v,k}$ using the key k_P . In addition, the λ_v -shares corresponding to the honest parties are computed honestly by \mathcal{S}_{Sh} .

Online Phase:

– If $P_i = P_0$, \mathcal{S}_{Sh} receives m_v of behalf of P_1, P_2, P_3 . \mathcal{S}_{Sh} sets flag = 1 if the received values mismatch. Else, it computes the input $v = m_v - \lambda_{v,1} - \lambda_{v,2} - \lambda_{v,3}$.

– If $P_i \neq P_0$, \mathcal{S}_{Sh} sets $v = 0$ by assigning $m_v = \lambda_{v,1} + \lambda_{v,2} + \lambda_{v,3}$.

If flag = 0 and $P_i = P_0$, \mathcal{S}_{Sh} invokes \mathcal{F}_{Sh} with input v on behalf of P_0 . Else it invokes \mathcal{F}_{Sh} with input \perp on behalf of P_0 .

Figure 26: Simulator \mathcal{S}_{Sh} for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 27.

Simulator \mathcal{S}_{Sh}

Offline Phase: \mathcal{S}_{Sh} emulates $\mathcal{F}_{\text{setup}}$ and gives the keys ($k_{01}, k_{12}, k_{13}, k_{012}, k_{013}, k_{123}$ and k_P) to \mathcal{A} . By emulating $\mathcal{F}_{\text{setup}}$, it learns the λ -values corresponding to input v .

– If $P_i = P_1$, \mathcal{S}_{Sh} computes $\lambda_{v,1}$ using the key k_P .

– If $P_i = P_k$ for $k \in \{0, 2, 3\}$, the λ_v -shares corresponding to the honest parties are computed honestly by \mathcal{S}_{Sh} .

Online Phase:

– If $P_i = P_1$, \mathcal{S}_{Sh} receives m_v of behalf of P_2, P_3 . \mathcal{S}_{Sh} sets flag = 1 if the received values mismatch. Else, it computes the input $v = m_v - \lambda_{v,1} - \lambda_{v,2} - \lambda_{v,3}$.

– If $P_i \neq P_1$, \mathcal{S}_{Sh} sets $v = 0$ by assigning $m_v = \lambda_{v,1} + \lambda_{v,2} + \lambda_{v,3}$. \mathcal{S}_{Sh} sends m_v to P_1 on behalf of the sender

If flag = 0 and $P_i = P_1$, \mathcal{S}_{Sh} invokes \mathcal{F}_{Sh} with input v on behalf of P_1 . Else it invokes \mathcal{F}_{Sh} with input \perp on behalf of P_1 .

Figure 27: Simulator \mathcal{S}_{Sh} for the case of corrupt P_1

b) *Verifiable Arithmetic/Boolean Sharing*: The ideal functionality realising protocol Π_{vSh} is presented in Fig. 28.

Functionality \mathcal{F}_{vSh}

\mathcal{F}_{vSh} interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . \mathcal{F}_{vSh} receives the input v from parties P_i, P_j while it receives \perp from the other parties. If the received values mismatch, then send \perp to every party, else proceed with the computation.

Computation of output: Randomly select $\lambda_{v,1}, \lambda_{v,2}, \lambda_{v,3}$ from \mathbb{Z}_{2^ℓ} and set $m_v = v + \lambda_{v,1} + \lambda_{v,2} + \lambda_{v,3}$. The output shares are set as:

$$\begin{aligned} \llbracket v \rrbracket_{P_0} &= (\lambda_{v,1}, \lambda_{v,2}, \lambda_{v,3}) & \llbracket v \rrbracket_{P_1} &= (m_v, \lambda_{v,2}, \lambda_{v,3}) \\ \llbracket v \rrbracket_{P_2} &= (m_v, \lambda_{v,3}, \lambda_{v,1}) & \llbracket v \rrbracket_{P_3} &= (m_v, \lambda_{v,1}, \lambda_{v,2}) \end{aligned}$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket v \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket v \rrbracket_{\mathcal{S}}$ denotes the share of v corresponding to the corrupt

party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket v \rrbracket_i$), where $\llbracket v \rrbracket_i$ denotes the share of v corresponding to the honest party P_i .

Figure 28: Functionality for protocol Π_{vSh}

The simulator for the case of corrupt P_0 appears in Fig. 29.

Simulator \mathcal{S}_{vSh}

Offline Phase: The simulation for the offline phase is similar to that of $\Pi_{\text{Sh}}(P_i, v)$ for the case of corrupt P_0 (Fig. 26).

Online Phase: \mathcal{S}_{vSh} proceeds similar to that of $\Pi_{\text{Sh}}(P_i, v)$ for the case of corrupt P_0 (Fig. 26). In addition, if $P_j = P_0$, it receives $H(m_v)$ from \mathcal{A} on behalf of P_1, P_2 , and P_3 . \mathcal{S}_{vSh} sets flag = 1 if the received values mismatch.

If flag = 0 and $P_j = P_0$, \mathcal{S}_{vSh} invokes \mathcal{F}_{vSh} with input v on behalf of P_0 . Else it invokes \mathcal{F}_{vSh} with input \perp on behalf of P_0 .

Figure 29: Simulator \mathcal{S}_{vSh} for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 30.

Simulator \mathcal{S}_{vSh}

Offline Phase: The simulation for the offline phase is similar to that of $\Pi_{\text{Sh}}(P_i, v)$ for the case of corrupt P_1 (Fig. 27).

Online Phase: \mathcal{S}_{vSh} proceeds similar to that of $\Pi_{\text{Sh}}(P_i, v)$ for the case of corrupt P_1 (Fig. 27). In addition, if $P_j = P_1$, it receives $H(m_v)$ from \mathcal{A} on behalf of P_2 , and P_3 . \mathcal{S}_{vSh} sets flag = 1 if the received values mismatch.

If flag = 0 and $P_i = P_1$, \mathcal{S}_{vSh} invokes \mathcal{F}_{vSh} with input v on behalf of P_1 . Else it invokes \mathcal{F}_{vSh} with input \perp on behalf of P_1 .

Figure 30: Simulator \mathcal{S}_{vSh} for the case of corrupt P_1

c) *Reconstruction Protocol:* The ideal functionality realising protocol Π_{Rec} is presented in Fig. 31.

Functionality \mathcal{F}_{Rec}

\mathcal{F}_{Rec} interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . \mathcal{F}_{Rec} receives the $\llbracket \cdot \rrbracket$ -shares of value v from party P_i for $i \in \{0, 1, 2, 3\}$. The shares are

$$\begin{aligned} \llbracket v \rrbracket_{P_0} &= (\lambda'_{v,1}, \lambda'_{v,2}, \lambda'_{v,3}) & \llbracket v \rrbracket_{P_1} &= (m'_v, \lambda''_{v,2}, \lambda''_{v,3}) \\ \llbracket v \rrbracket_{P_2} &= (m''_v, \lambda'''_{v,3}, \lambda'''_{v,1}) & \llbracket v \rrbracket_{P_3} &= (m'''_v, \lambda'''_{v,1}, \lambda'''_{v,2}) \end{aligned}$$

\mathcal{F}_{Rec} sends \perp to every party if either of the following condition is met: i) $\lambda'_{v,1} \neq \lambda''_{v,1} \neq \lambda'''_{v,1}$, ii) $\lambda'_{v,2} \neq \lambda''_{v,2} \neq \lambda'''_{v,2}$, iii) $\lambda'_{v,3} \neq \lambda''_{v,3} \neq \lambda'''_{v,3}$ or iv) $m'_v \neq m''_v \neq m'''_v$. Else it proceeds with the computation.

Computation of output: Set $v = m_v - \lambda_{v,1} - \lambda_{v,2} - \lambda_{v,3}$.

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, v) to the adversary \mathcal{S} .

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, v).

Figure 31: Functionality for protocol Π_{Rec}

The simulator for the case of corrupt P_0 appears in Fig. 32. As mentioned at the beginning of this section, \mathcal{S} knows all of the intermediate values and the output of the ckt. \mathcal{S} uses this information to simulate the Π_{Rec} protocol.

Simulator \mathcal{S}_{Rec}

Online Phase:

- \mathcal{S}_{Rec} computes $m_v = v + \lambda_{v,1} + \lambda_{v,2} + \lambda_{v,3}$. It then sends m_v and $H(m_v)$ to \mathcal{A} on behalf of P_1 and P_2 respectively.
- \mathcal{S}_{Rec} receives $H(\lambda'_{v,1})$, $H(\lambda'_{v,2})$ and $H(\lambda'_{v,3})$ from \mathcal{A} on behalf of P_1, P_2 and P_3 respectively. \mathcal{S}_{Rec} sets flag = 1 if any of the received hash values is inconsistent.

If flag = 0, \mathcal{S}_{Rec} invokes \mathcal{F}_{Rec} with input $(\lambda_{v,1}, \lambda_{v,2}, \lambda_{v,3})$ on behalf of P_0 . Else it invokes \mathcal{F}_{Rec} with input \perp on behalf of P_0 .

Figure 32: Simulator \mathcal{S}_{Rec} for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 33.

Simulator \mathcal{S}_{Rec}

Online Phase:

- \mathcal{S}_{Rec} sends $\lambda_{v,1}$ and $H(\lambda_{v,1})$ to \mathcal{A} on behalf of P_2 and P_0 respectively.
- \mathcal{S}_{Rec} receives $\lambda'_{v,3}$ and $H(m'_v)$ from \mathcal{A} on behalf of P_3 and P_0 respectively. \mathcal{S}_{Rec} sets flag = 1 if any of the received values is inconsistent.

If flag = 0, \mathcal{S}_{Rec} invokes \mathcal{F}_{Rec} with input $(m_v, \lambda_{v,2}, \lambda_{v,3})$ on behalf of P_1 . Else it invokes \mathcal{F}_{Rec} with input \perp on behalf of P_1 .

Figure 33: Simulator \mathcal{S}_{Rec} for the case of corrupt P_1

d) *Multiplication:* The ideal functionality realising protocol Π_{Mult} is presented in Fig. 34.

Functionality $\mathcal{F}_{\text{Mult}}$

$\mathcal{F}_{\text{Mult}}$ interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . $\mathcal{F}_{\text{Mult}}$ receives $\llbracket \cdot \rrbracket$ -shares of values x and y from the parties as input. If $\mathcal{F}_{\text{Mult}}$ receives \perp from \mathcal{S} , then send \perp to every party, else proceed with the computation.

Computation of output: Compute $x = m_x - \lambda_{x,1} - \lambda_{x,2} - \lambda_{x,3}$, $y = m_y - \lambda_{y,1} - \lambda_{y,2} - \lambda_{y,3}$ and set $z = xy$. Randomly select $\lambda_{z,1}, \lambda_{z,2}, \lambda_{z,3}$ from \mathbb{Z}_{2^ℓ} and set $m_z = z + \lambda_{z,1} + \lambda_{z,2} + \lambda_{z,3}$. The output shares are set as:

$$\begin{aligned} \llbracket z \rrbracket_{P_0} &= (\lambda_{z,1}, \lambda_{z,2}, \lambda_{z,3}) & \llbracket z \rrbracket_{P_1} &= (m_z, \lambda_{z,2}, \lambda_{z,3}) \\ \llbracket z \rrbracket_{P_2} &= (m_z, \lambda_{z,3}, \lambda_{z,1}) & \llbracket z \rrbracket_{P_3} &= (m_z, \lambda_{z,1}, \lambda_{z,2}) \end{aligned}$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket z \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket z \rrbracket_{\mathcal{S}}$ denotes the share of z corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket z \rrbracket_i$), where $\llbracket z \rrbracket_i$ denotes the share of z corresponding to the honest party P_i .

Figure 34: Functionality for protocol Π_{Mult}

The simulator for the case of corrupt P_0 appears in Fig. 35.

Simulator $\mathcal{S}_{\text{Mult}}$

Offline Phase:

- $\mathcal{S}_{\text{Mult}}$ samples $\lambda_{z,j}$ for $j \in \{1, 2, 3\}$ honestly using the shared keys obtained.
- $\mathcal{S}_{\text{Mult}}$ emulates $\mathcal{F}_{\text{Zero}}$ functionality and gives the values A, B , and Γ to \mathcal{A} .
- $\mathcal{S}_{\text{Mult}}$ receives $H(\gamma_{xy,1}), H(\gamma_{xy,2})$ and $H(\gamma_{xy,3})$ from \mathcal{A} on behalf of P_2, P_3 and P_1 respectively. $\mathcal{S}_{\text{Mult}}$ sets flag = 1 if any of the received values is inconsistent.

Online Phase: There is nothing to simulate as P_0 has no role during the online phase.

If flag = 0, $\mathcal{S}_{\text{Mult}}$ invokes $\mathcal{F}_{\text{Mult}}$ with input $(\llbracket x \rrbracket_{P_0}, \llbracket y \rrbracket_{P_0})$ on behalf of P_0 . Else it invokes $\mathcal{F}_{\text{Mult}}$ with input \perp on behalf of P_0 .

Figure 35: Simulator for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 36.

Simulator $\mathcal{S}_{\text{Mult}}$

Offline Phase:

- $\mathcal{S}_{\text{Mult}}$ samples $\lambda_{z,j}$ for $j \in \{2, 3\}$ honestly using the shared keys obtained.
- $\mathcal{S}_{\text{Mult}}$ emulates $\mathcal{F}_{\text{Zero}}$ functionality and gives the value A to \mathcal{A} .
- $\mathcal{S}_{\text{Mult}}$ sends $\gamma_{xy,3}$ and $H(\gamma_{xy,3})$ to \mathcal{A} on behalf of P_2 and P_0 respectively. It receives $\gamma_{xy,2}$ from \mathcal{A} on behalf of P_3 and sets flag = 1 if the received value is inconsistent.

Online Phase:

- $\mathcal{S}_{\text{Mult}}$ simulates the computation of m'_z shares honestly on behalf of P_2 and P_3 .
- $\mathcal{S}_{\text{Mult}}$ sends $m'_{z,1}$ and $H(m'_{z,1})$ to \mathcal{A} on behalf of P_2 and P_3 respectively. It receives $m'_{z,3}$ and $H(m'_{z,2})$ from \mathcal{A} on behalf of P_3 and P_2 respectively. $\mathcal{S}_{\text{Mult}}$ sets flag = 1 if any of the received values is inconsistent.

If flag = 0, $\mathcal{S}_{\text{Mult}}$ invokes $\mathcal{F}_{\text{Mult}}$ with input $(\llbracket x \rrbracket_{P_1}, \llbracket y \rrbracket_{P_1})$ on behalf of P_1 . Else it invokes $\mathcal{F}_{\text{Mult}}$ with input \perp on behalf of P_1 .

Figure 36: Simulator for the case of corrupt P_1

e) *Dot Product:* The ideal functionality realising protocol Π_{DotP} is presented in Fig. 37.

Functionality $\mathcal{F}_{\text{DotP}}$

$\mathcal{F}_{\text{DotP}}$ interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . $\mathcal{F}_{\text{DotP}}$ receives $\llbracket \cdot \rrbracket$ -shares of vectors \vec{x} and \vec{y} from the parties as input. Here \vec{x} and \vec{y} are d -length vectors. If $\mathcal{F}_{\text{DotP}}$ receives \perp from \mathcal{S} , then send \perp to every party, else proceed with the computation.

Computation of output: Compute $x_i = m_{x_i} - \lambda_{x_i,1} - \lambda_{x_i,2} - \lambda_{x_i,3}$, $y_i = m_{y_i} - \lambda_{y_i,1} - \lambda_{y_i,2} - \lambda_{y_i,3}$ for $i \in [d]$ and set $z = \sum_{i=1}^d x_i y_i$. Randomly select $\lambda_{z,1}, \lambda_{z,2}, \lambda_{z,3}$ from \mathbb{Z}_{2^ℓ} and set $m_z = z + \lambda_{z,1} + \lambda_{z,2} + \lambda_{z,3}$. The output shares are set as:

$$\llbracket z \rrbracket_{P_0} = (\lambda_{z,1}, \lambda_{z,2}, \lambda_{z,3}) \quad \llbracket z \rrbracket_{P_1} = (m_z, \lambda_{z,2}, \lambda_{z,3})$$

$$\llbracket z \rrbracket_{P_2} = (m_z, \lambda_{z,3}, \lambda_{z,1}) \quad \llbracket z \rrbracket_{P_3} = (m_z, \lambda_{z,1}, \lambda_{z,2})$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket z \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket z \rrbracket_{\mathcal{S}}$ denotes the share of z corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket z \rrbracket_i$), where $\llbracket z \rrbracket_i$ denotes the share of z corresponding to the honest party P_i .

Figure 37: Functionality for protocol Π_{Sh}

The simulator for the case of corrupt P_0 appears in Fig. 38.

Simulator $\mathcal{S}_{\text{DotP}}$

Offline Phase:

- $\mathcal{S}_{\text{DotP}}$ samples $\lambda_{z,j}$ for $j \in \{1, 2, 3\}$ honestly using the shared keys obtained.
- $\mathcal{S}_{\text{DotP}}$ emulates $\mathcal{F}_{\text{Zero}}$ functionality and gives the values A, B , and Γ to \mathcal{A} .
- $\mathcal{S}_{\text{DotP}}$ receives $H(\gamma_{xy,1}), H(\gamma_{xy,2})$ and $H(\gamma_{xy,3})$ from \mathcal{A} on behalf of P_2, P_3 and P_1 respectively. $\mathcal{S}_{\text{DotP}}$ sets flag = 1 if any of the received values is inconsistent.

Online Phase: There is nothing to simulate as P_0 has no role during the online phase.

If flag = 0, $\mathcal{S}_{\text{DotP}}$ invokes $\mathcal{F}_{\text{DotP}}$ with input $(\{\llbracket x_i \rrbracket_{P_0}, \llbracket y_i \rrbracket_{P_0}\}_{i \in [d]})$ on behalf of P_0 . Else it invokes $\mathcal{F}_{\text{DotP}}$ with input \perp on behalf of P_0 .

Figure 38: Simulator for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 39.

Simulator $\mathcal{S}_{\text{DotP}}$

Offline Phase:

- $\mathcal{S}_{\text{DotP}}$ samples $\lambda_{z,j}$ for $j \in \{2, 3\}$ honestly using the shared keys obtained.
- $\mathcal{S}_{\text{DotP}}$ emulates $\mathcal{F}_{\text{Zero}}$ functionality and gives the value A to \mathcal{A} .
- $\mathcal{S}_{\text{DotP}}$ sends $\gamma_{xy,3}$ and $H(\gamma_{xy,3})$ to \mathcal{A} on behalf of P_2 and P_0 respectively. It receives $\gamma_{xy,2}$ from \mathcal{A} on behalf of P_3 and sets flag = 1 if the received value is inconsistent.

Online Phase:

- $\mathcal{S}_{\text{DotP}}$ simulates the computation of m'_z shares honestly on behalf of P_2 and P_3 .
- $\mathcal{S}_{\text{DotP}}$ sends $m'_{z,1}$ and $H(m'_{z,1})$ to \mathcal{A} on behalf of P_2 and P_3 respectively. It receives $m'_{z,3}$ and $H(m'_{z,2})$ from \mathcal{A} on behalf of P_3 and P_2 respectively. $\mathcal{S}_{\text{DotP}}$ sets $\text{flag} = 1$ if any of the received values is inconsistent.

If $\text{flag} = 0$, $\mathcal{S}_{\text{DotP}}$ invokes $\mathcal{F}_{\text{DotP}}$ with input $(\{\llbracket x_i \rrbracket_{P_1}, \llbracket y_i \rrbracket_{P_1} \}_{i \in [d]})$ on behalf of P_1 . Else it invokes $\mathcal{F}_{\text{DotP}}$ with input \perp on behalf of P_1 .

Figure 39: Simulator for the case of corrupt P_1

f) *Bit to Arithmetic Sharing (Bit2A)*: The ideal functionality realising protocol Π_{Bit2A} is presented in Fig. 40.

Functionality $\mathcal{F}_{\text{Bit2A}}$

$\mathcal{F}_{\text{Bit2A}}$ interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . $\mathcal{F}_{\text{Bit2A}}$ receives $\llbracket \cdot \rrbracket^{\text{B}}$ -share of a bit b from the parties as input. If $\mathcal{F}_{\text{Bit2A}}$ receives \perp from \mathcal{S} , then send \perp to every party, else proceed with the computation.

Computation of output: Compute $b = m_b \oplus \lambda_{b,1} \oplus \lambda_{b,2} \oplus \lambda_{b,3}$. Let b' denotes the value of bit b over an arithmetic ring \mathbb{Z}_{2^ℓ} . Randomly select $\lambda'_{b,1}, \lambda'_{b,2}, \lambda'_{b,3}$ from \mathbb{Z}_{2^ℓ} and set $m'_b = b' + \lambda'_{b,1} + \lambda'_{b,2} + \lambda'_{b,3}$. The output shares are set as:

$$\begin{aligned} \llbracket b \rrbracket_{P_0}' &= (\lambda'_{b,1}, \lambda'_{b,2}, \lambda'_{b,3}) & \llbracket b \rrbracket_{P_1}' &= (m'_b, \lambda'_{b,2}, \lambda'_{b,3}) \\ \llbracket b \rrbracket_{P_2}' &= (m'_b, \lambda'_{b,3}, \lambda'_{b,1}) & \llbracket b \rrbracket_{P_3}' &= (m'_b, \lambda'_{b,1}, \lambda'_{b,2}) \end{aligned}$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket b' \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket b' \rrbracket_{\mathcal{S}}$ denotes the share of b' corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket b' \rrbracket_i$), where $\llbracket b' \rrbracket_i$ denotes the share of b' corresponding to the honest party P_i .

Figure 40: Functionality for protocol Π_{Bit2A}

The simulator for the case of corrupt P_0 appears in Fig. 41.

Simulator $\mathcal{S}_{\text{Bit2A}}$

Offline Phase:

- Corresponding to the invocation of $\Pi_{\text{aSh}}(P_0, u)$ protocol, $\mathcal{S}_{\text{Bit2A}}$ receives v_3 from \mathcal{A} on behalf of both P_1 and P_2 . $\mathcal{S}_{\text{Bit2A}}$ sets $\text{flag} = 1$ if the received values mismatch.
- $\mathcal{S}_{\text{Bit2A}}$ performs the check honestly and sets $\text{flag} = 1$ if the verification fails.

Online Phase: The steps corresponding to Π_{vSh} and Π_{Mult} are simulated similar to \mathcal{S}_{vSh} (Fig. 29) and $\mathcal{S}_{\text{Mult}}$ (Fig. 35) respectively, for the case of corrupt P_0 .

If $\text{flag} = 0$, $\mathcal{S}_{\text{Bit2A}}$ invokes $\mathcal{F}_{\text{Bit2A}}$ with input $(\llbracket b \rrbracket_{P_0}^{\text{B}})$ on behalf of P_0 . Else it invokes $\mathcal{F}_{\text{Bit2A}}$ with input \perp on behalf of P_0 .

Figure 41: Simulator for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 42.

Simulator $\mathcal{S}_{\text{Bit2A}}$

Offline Phase:

- Corresponding to the invocation of $\Pi_{\text{aSh}}(P_0, u)$ protocol, $\mathcal{S}_{\text{Bit2A}}$ sends v_3 to \mathcal{A} on behalf of P_0 . It then sends $H(v_3)$ to \mathcal{A} on behalf of P_2 and receives $H(v'_3)$ back. $\mathcal{S}_{\text{Bit2A}}$ sets $\text{flag} = 1$ if it receives inconsistent hash value.
- $\mathcal{S}_{\text{Bit2A}}$ receives (x_1, y_1) from \mathcal{A} on behalf of P_3 , performs the check honestly and sets $\text{flag} = 1$ if the verification fails.

Online Phase: The steps corresponding to Π_{vSh} and Π_{Mult} are simulated similar to \mathcal{S}_{vSh} (Fig. 30) and $\mathcal{S}_{\text{Mult}}$ (Fig. 36) respectively, for the case of corrupt P_1 .

If $\text{flag} = 0$, $\mathcal{S}_{\text{Bit2A}}$ invokes $\mathcal{F}_{\text{Bit2A}}$ with input $(\llbracket b \rrbracket_{P_1}^{\text{B}})$ on behalf of P_1 . Else it invokes $\mathcal{F}_{\text{Bit2A}}$ with input \perp on behalf of P_1 .

Figure 42: Simulator for the case of corrupt P_1

g) *Bit Injection (BitInj)*: The ideal functionality realising protocol Π_{BitInj} is presented in Fig. 43.

Functionality $\mathcal{F}_{\text{BitInj}}$

$\mathcal{F}_{\text{BitInj}}$ interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . $\mathcal{F}_{\text{BitInj}}$ receives $(\llbracket b \rrbracket^{\text{B}}, \llbracket v \rrbracket)$ from the parties as input. If $\mathcal{F}_{\text{BitInj}}$ receives \perp from \mathcal{S} , then send \perp to every party, else proceed with the computation.

Computation of output: Compute $b = m_b \oplus \lambda_{b,1} \oplus \lambda_{b,2} \oplus \lambda_{b,3}$, $v = m_v - \lambda_{v,1} - \lambda_{v,2} - \lambda_{v,3}$ and set $(bv) = b \cdot v$. Randomly select $\lambda_{(bv),1}, \lambda_{(bv),2}, \lambda_{(bv),3}$ from \mathbb{Z}_{2^ℓ} and set $m_{(bv)} = (bv) + \lambda_{(bv),1} + \lambda_{(bv),2} + \lambda_{(bv),3}$. The output shares are set as:

$$\begin{aligned} \llbracket (bv) \rrbracket_{P_0} &= (\lambda_{(bv),1}, \lambda_{(bv),2}, \lambda_{(bv),3}) \\ \llbracket (bv) \rrbracket_{P_1} &= (m_{(bv)}, \lambda_{(bv),2}, \lambda_{(bv),3}) \\ \llbracket (bv) \rrbracket_{P_2} &= (m_{(bv)}, \lambda_{(bv),3}, \lambda_{(bv),1}) \\ \llbracket (bv) \rrbracket_{P_3} &= (m_{(bv)}, \lambda_{(bv),1}, \lambda_{(bv),2}) \end{aligned}$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket (bv) \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket (bv) \rrbracket_{\mathcal{S}}$ denotes the share of (bv) corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket (bv) \rrbracket_i$), where $\llbracket (bv) \rrbracket_i$ denotes the share of (bv) corresponding to the honest party P_i .

Figure 43: Functionality for protocol Π_{BitInj}

The simulator for the case of corrupt P_0 appears in Fig. 44.

Simulator $\mathcal{S}_{\text{BitInj}}$

Offline Phase:

- Corresponding to the invocation of $\Pi_{\text{aSh}}(P_0, y_j)$ protocol, $\mathcal{S}_{\text{BitInj}}$ receives $y_{j,3}$ from \mathcal{A} on behalf of both P_1 and P_2 . $\mathcal{S}_{\text{BitInj}}$ sets flag = 1 if the received values mismatch.
- $\mathcal{S}_{\text{BitInj}}$ performs the check honestly and sets flag = 1 if the verification fails.

Online Phase: The steps corresponding to Π_{vSh} are simulated similar to \mathcal{S}_{vSh} (Fig. 29), for the case of corrupt P_0 .

If flag = 0, $\mathcal{S}_{\text{BitInj}}$ invokes $\mathcal{F}_{\text{BitInj}}$ with input $(\llbracket b \rrbracket_{P_0}^B, \llbracket v \rrbracket_{P_0})$ on behalf of P_0 . Else it invokes $\mathcal{F}_{\text{BitInj}}$ with input \perp on behalf of P_0 .

Figure 44: Simulator for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 45.

Simulator $\mathcal{S}_{\text{BitInj}}$

Offline Phase:

- Corresponding to the invocation of $\Pi_{\text{aSh}}(P_0, u)$ protocol, $\mathcal{S}_{\text{BitInj}}$ sends $y_{j,3}$ to \mathcal{A} on behalf of P_0 . It then sends $H(y_{j,3})$ to \mathcal{A} on behalf of P_2 and receives $H(y'_{j,3})$ back. $\mathcal{S}_{\text{BitInj}}$ sets flag = 1 if it receives inconsistent hash value.
- $\mathcal{S}_{\text{BitInj}}$ emulates $\mathcal{F}_{\text{Zero}}$ functionality and gives A to \mathcal{A} .
- $\mathcal{S}_{\text{BitInj}}$ receives z_2 from \mathcal{A} on behalf of P_3 , performs the check honestly and sets flag = 1 if the verification fails.

Online Phase: The steps corresponding to Π_{vSh} are simulated similar to \mathcal{S}_{vSh} (Fig. 30), for the case of corrupt P_1 .

If flag = 0, $\mathcal{S}_{\text{BitInj}}$ invokes $\mathcal{F}_{\text{BitInj}}$ with input $(\llbracket b \rrbracket_{P_1}^B, \llbracket v \rrbracket_{P_1})$ on behalf of P_1 . Else it invokes $\mathcal{F}_{\text{BitInj}}$ with input \perp on behalf of P_1 .

Figure 45: Simulator for the case of corrupt P_1

h) Multiplication with Truncation: The ideal functionality realising protocol Π_{MultTr} is presented in Fig. 46.

Functionality $\mathcal{F}_{\text{MultTr}}$

$\mathcal{F}_{\text{MultTr}}$ interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . $\mathcal{F}_{\text{MultTr}}$ receives $\llbracket \cdot \rrbracket$ -shares of values x and y from the parties as input. If $\mathcal{F}_{\text{MultTr}}$ receives \perp from \mathcal{S} , then send \perp to every party, else proceed with the computation.

Computation of output: Compute $x = m_x - \lambda_{x,1} - \lambda_{x,2} - \lambda_{x,3}$, $y = m_y - \lambda_{y,1} - \lambda_{y,2} - \lambda_{y,3}$ and set $z^t = (xy)^t$. Randomly select $\lambda_{z^t,1}, \lambda_{z^t,2}, \lambda_{z^t,3}$ from \mathbb{Z}_{2^ℓ} and set $m_{z^t} = z^t + \lambda_{z^t,1} + \lambda_{z^t,2} + \lambda_{z^t,3}$. The output shares are set as:

$$\llbracket z^t \rrbracket_{P_0} = (\lambda_{z^t,1}, \lambda_{z^t,2}, \lambda_{z^t,3}) \quad \llbracket z^t \rrbracket_{P_1} = (m_{z^t}, \lambda_{z^t,2}, \lambda_{z^t,3})$$

$$\llbracket z^t \rrbracket_{P_2} = (m_{z^t}, \lambda_{z^t,3}, \lambda_{z^t,1}) \quad \llbracket z^t \rrbracket_{P_3} = (m_{z^t}, \lambda_{z^t,1}, \lambda_{z^t,2})$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket z^t \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket z^t \rrbracket_{\mathcal{S}}$ denotes the share of z^t corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket z^t \rrbracket_i$), where $\llbracket z^t \rrbracket_i$ denotes the share of z^t corresponding to the honest party P_i .

Figure 46: Functionality for protocol Π_{MultTr}

The simulator for the case of corrupt P_0 appears in Fig. 47.

Simulator $\mathcal{S}_{\text{MultTr}}$

Offline Phase:

- The steps corresponding to offline phase of Π_{Mult} are simulated similar to offline phase of $\mathcal{S}_{\text{Mult}}$ (Fig. 35), for the case of corrupt P_0 .
- Corresponding to the invocation of $\Pi_{\text{aSh}}(P_0, r^t)$ protocol, $\mathcal{S}_{\text{MultTr}}$ receives r_3^t from \mathcal{A} on behalf of both P_1 and P_2 . $\mathcal{S}_{\text{MultTr}}$ sets flag = 1 if the received values mismatch.
- $\mathcal{S}_{\text{MultTr}}$ performs the check honestly and sets flag = 1 if the verification fails.

Online Phase: There is nothing to simulate as P_0 has no role during the online phase.

If flag = 0, $\mathcal{S}_{\text{MultTr}}$ invokes $\mathcal{F}_{\text{MultTr}}$ with input $(\llbracket x \rrbracket_{P_0}, \llbracket y \rrbracket_{P_0})$ on behalf of P_0 . Else it invokes $\mathcal{F}_{\text{MultTr}}$ with input \perp on behalf of P_0 .

Figure 47: Simulator for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 48.

Simulator $\mathcal{S}_{\text{MultTr}}$

Offline Phase:

- The steps corresponding to offline phase of Π_{Mult} are simulated similar to offline phase of $\mathcal{S}_{\text{Mult}}$ (Fig. 36), for the case of corrupt P_1 .
- Corresponding to the invocation of $\Pi_{\text{aSh}}(P_0, r^t)$ protocol, $\mathcal{S}_{\text{MultTr}}$ sends r_3^t to \mathcal{A} on behalf of P_0 . It then sends $H(r_3^t)$ to \mathcal{A} on behalf of P_2 and receives $H(r_3'^t)$ back. $\mathcal{S}_{\text{MultTr}}$ sets flag = 1 if it receives inconsistent hash value.
- $\mathcal{S}_{\text{MultTr}}$ receives $(m_1, H(c))$ from \mathcal{A} on behalf of P_2 , performs the check honestly and sets flag = 1 if the verification fails.

Online Phase:

- $\mathcal{S}_{\text{MultTr}}$ simulates the computation of m'_z shares honestly on behalf of P_2 and P_3 .
- $\mathcal{S}_{\text{MultTr}}$ sends $m'_{z,1}$ and $H(m'_{z,1})$ to \mathcal{A} on behalf of P_2 and P_3 respectively. It receives $m'_{z,3}$ and $H(m'_{z,2})$ from \mathcal{A} on behalf of P_3 and P_2 respectively. $\mathcal{S}_{\text{MultTr}}$ sets flag = 1 if any of the received values is inconsistent.

If flag = 0, $\mathcal{S}_{\text{MultTr}}$ invokes $\mathcal{F}_{\text{MultTr}}$ with input $(\llbracket x \rrbracket_{P_1}, \llbracket y \rrbracket_{P_1})$ on behalf of P_1 . Else it invokes $\mathcal{F}_{\text{MultTr}}$ with input \perp on behalf of P_1 .

Figure 48: Simulator for the case of corrupt P_1

i) Secure Comparison: The ideal functionality realising protocol Π_{BitExt} is presented in Fig. 49.

Functionality $\mathcal{F}_{\text{BitExt}}$

$\mathcal{F}_{\text{BitExt}}$ interacts with the parties in \mathcal{P} and the adversary \mathcal{S} . $\mathcal{F}_{\text{BitExt}}$ receives $\llbracket \cdot \rrbracket$ -share of value v from the parties as input. If $\mathcal{F}_{\text{BitExt}}$ receives \perp from \mathcal{S} , then send \perp to every party, else proceed with the computation.

Computation of output: Compute $v = m_v - \lambda_{v,1} - \lambda_{v,2} - \lambda_{v,3}$ and set $b = \text{msb}(v)$ where msb denotes the most significant bit. Randomly select $\lambda_{b,1}, \lambda_{b,2}, \lambda_{b,3}$ from \mathbb{Z}_{2^1} and set $m_b = b \oplus \lambda_{b,1} \oplus \lambda_{b,2} \oplus \lambda_{b,3}$. The output shares are set as:

$$\llbracket b \rrbracket_{P_0}^B = (\lambda_{b,1}, \lambda_{b,2}, \lambda_{b,3}) \quad \llbracket b \rrbracket_{P_1}^B = (m_b, \lambda_{b,2}, \lambda_{b,3})$$

$$\llbracket b \rrbracket_{P_2}^B = (m_b, \lambda_{b,3}, \lambda_{b,1}) \quad \llbracket b \rrbracket_{P_3}^B = (m_b, \lambda_{b,1}, \lambda_{b,2})$$

Output to adversary: If \mathcal{S} sends abort, then send (Output, \perp) to all the parties. Otherwise, send (Output, $\llbracket b \rrbracket_{\mathcal{S}}$) to the adversary \mathcal{S} , where $\llbracket b \rrbracket_{\mathcal{S}}$ denotes the share of b corresponding to the corrupt party.

Output to selected honest parties: Receive (select, $\{I\}$) from adversary \mathcal{S} , where $\{I\}$ denotes a subset of the honest parties. If an honest party P_i belongs to I , send (Output, \perp), else send (Output, $\llbracket b \rrbracket_i$), where $\llbracket b \rrbracket_i$ denotes the share of b corresponding to the honest party P_i .

Figure 49: Functionality for protocol Π_{BitExt}

The simulator for the case of corrupt P_0 appears in Fig. 50.

Simulator $\mathcal{S}_{\text{BitExt}}$

Offline Phase:

- $\mathcal{S}_{\text{BitExt}}$ samples a random r on behalf of P_1, P_2 and set $x = \text{msb}(r)$.
- The steps corresponding to Π_{vSh} are simulated similar to \mathcal{S}_{vSh} (Fig. 29), for the case of corrupt P_0 .

Online Phase:

- The steps corresponding to Π_{Mult} and Π_{Rec} are simulated similar to $\mathcal{S}_{\text{Mult}}$ (Fig. 35) and \mathcal{S}_{Rec} (Fig. 32) respectively, for the case of corrupt P_0 .
- The steps corresponding to Π_{vSh} is simulated similar to \mathcal{S}_{vSh} (Fig. 29), for the case of corrupt P_0 .

If flag = 0, $\mathcal{S}_{\text{BitExt}}$ invokes $\mathcal{F}_{\text{BitExt}}$ with input $(\lambda_{v,1}, \lambda_{v,2}, \lambda_{v,3})$ on behalf of P_0 . Else it invokes $\mathcal{F}_{\text{BitExt}}$ with input \perp on behalf of P_0 .

Figure 50: Simulator for the case of corrupt P_0

The simulator for the case of corrupt P_1 appears in Fig. 51.

Simulator $\mathcal{S}_{\text{BitExt}}$

Offline Phase:

- $\mathcal{S}_{\text{BitExt}}$ samples a random r on behalf of P_2 and set $x = \text{msb}(r)$.
- The steps corresponding to Π_{vSh} are simulated similar to \mathcal{S}_{vSh} (Fig. 30), for the case of corrupt P_1 .

Online Phase:

- The steps corresponding to Π_{Mult} and Π_{Rec} are simulated similar

to $\mathcal{S}_{\text{Mult}}$ (Fig. 36) and \mathcal{S}_{Rec} (Fig. 33) respectively, for the case of corrupt P_1 .

- The steps corresponding to Π_{vSh} is simulated similar to \mathcal{S}_{vSh} (Fig. 30), for the case of corrupt P_1 .

If flag = 0, $\mathcal{S}_{\text{BitExt}}$ invokes $\mathcal{F}_{\text{BitExt}}$ with input $(m_v, \lambda_{v,2}, \lambda_{v,3})$ on behalf of P_1 . Else it invokes $\mathcal{F}_{\text{BitExt}}$ with input \perp on behalf of P_1 .

Figure 51: Simulator for the case of corrupt P_1